

FIG. 1

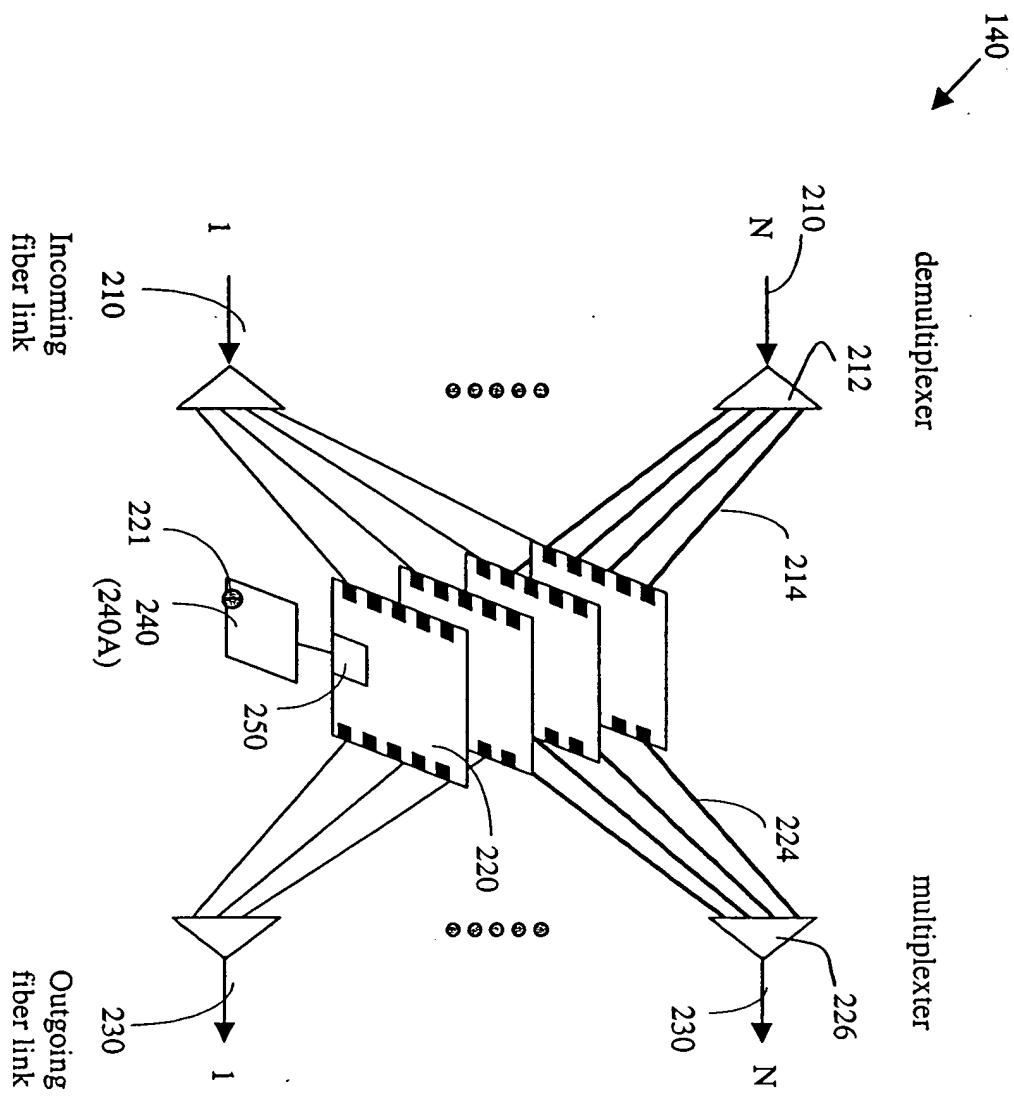


FIG. 2

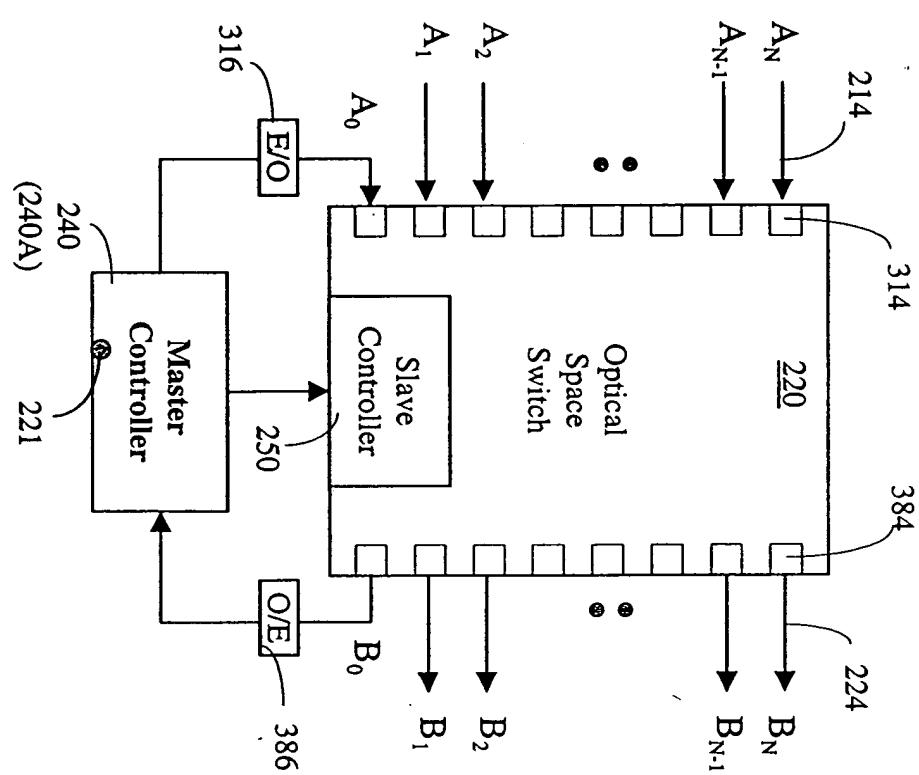


FIG. 3

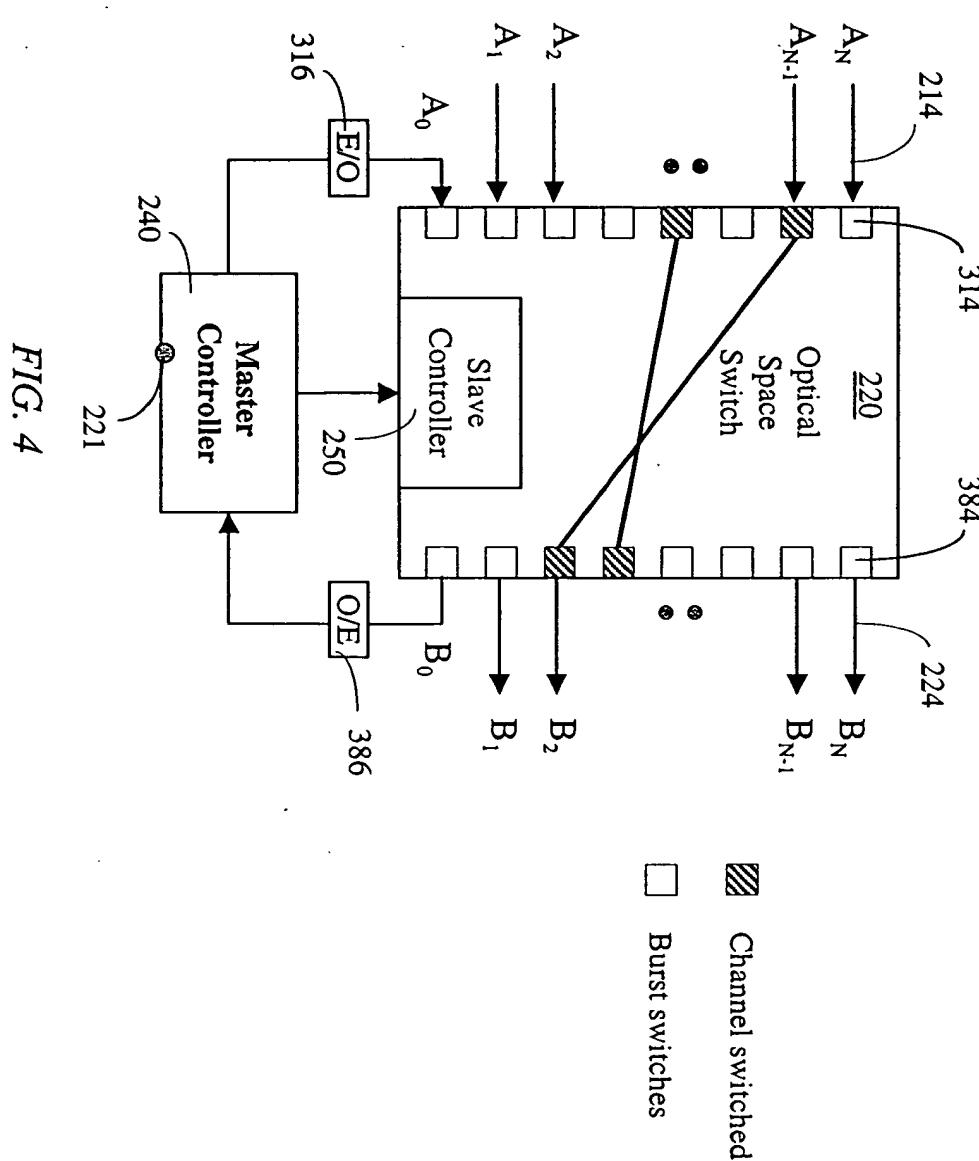


FIG. 4

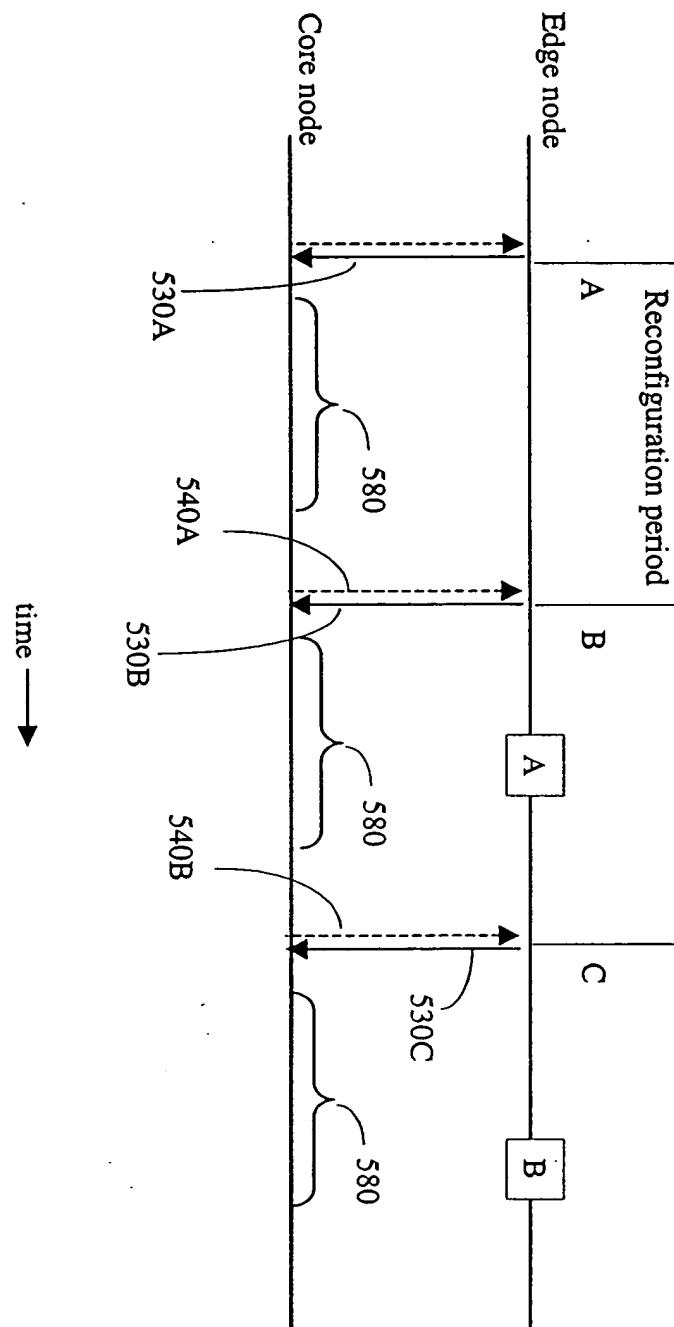


FIG. 5

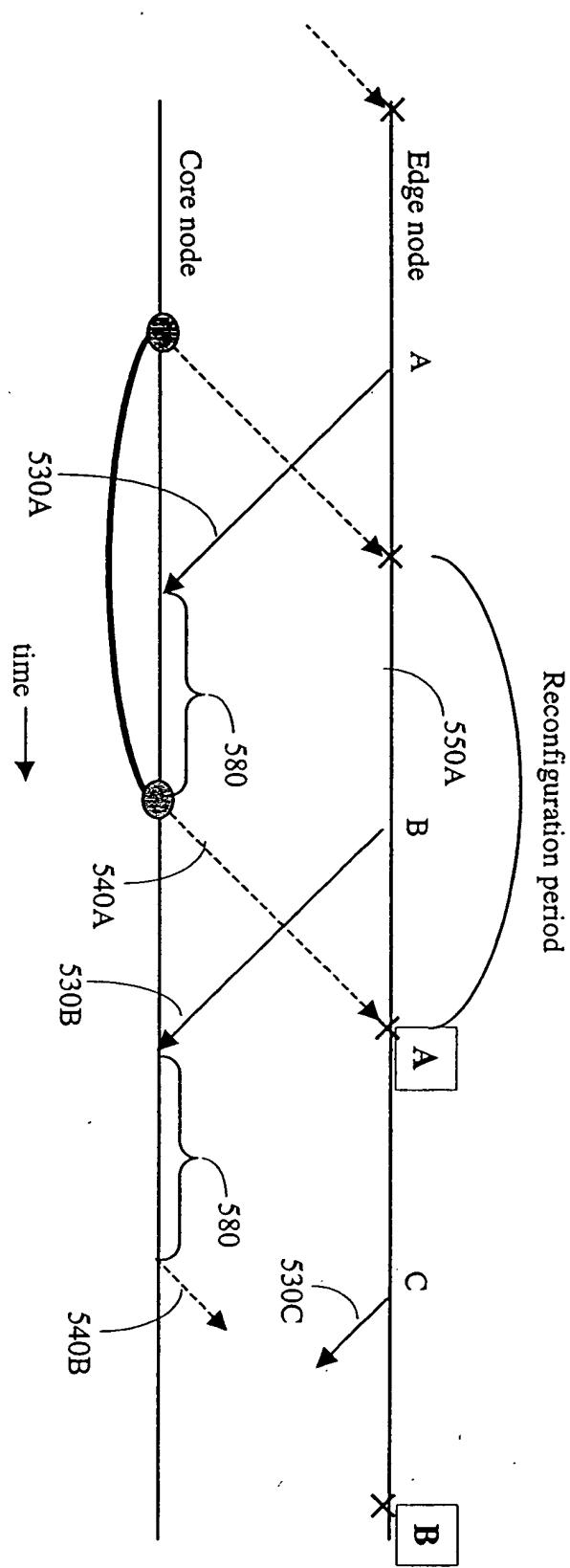


FIG. 6

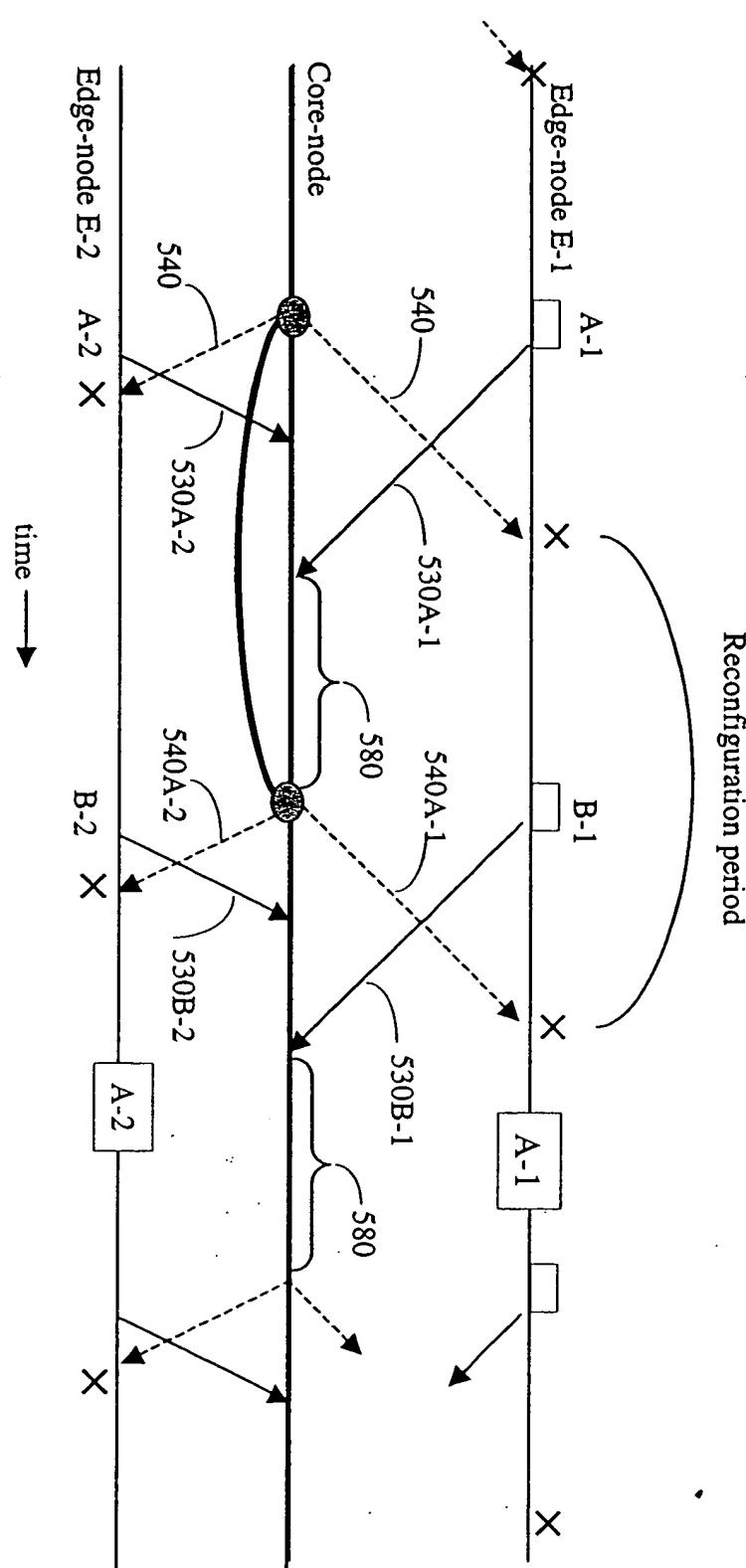


FIG. 7

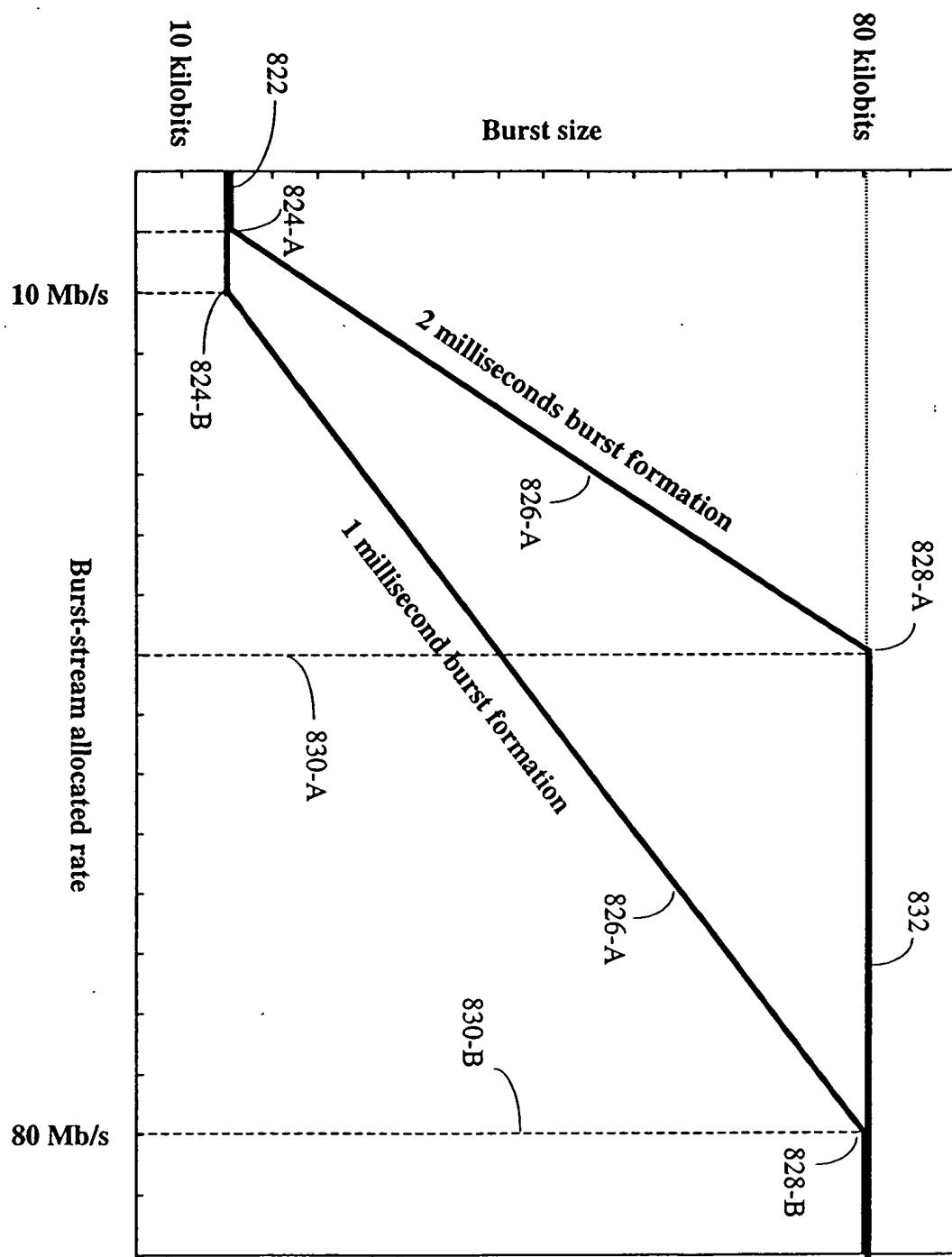
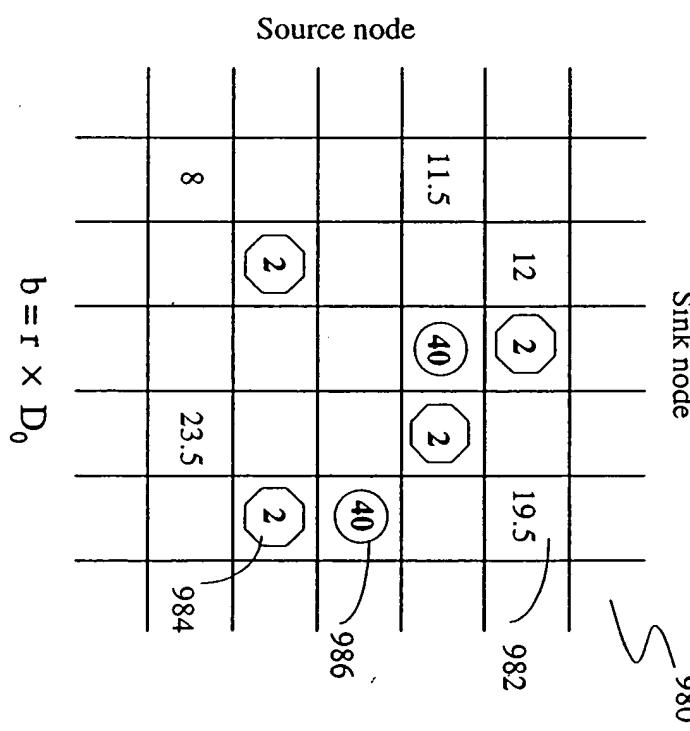
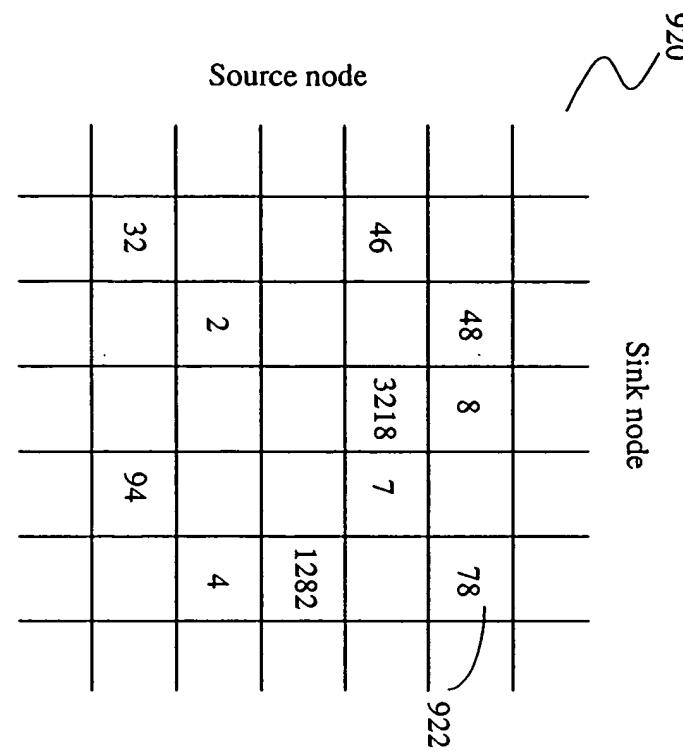


FIG. 8



$$b = r \times D_0$$

FIG. 9

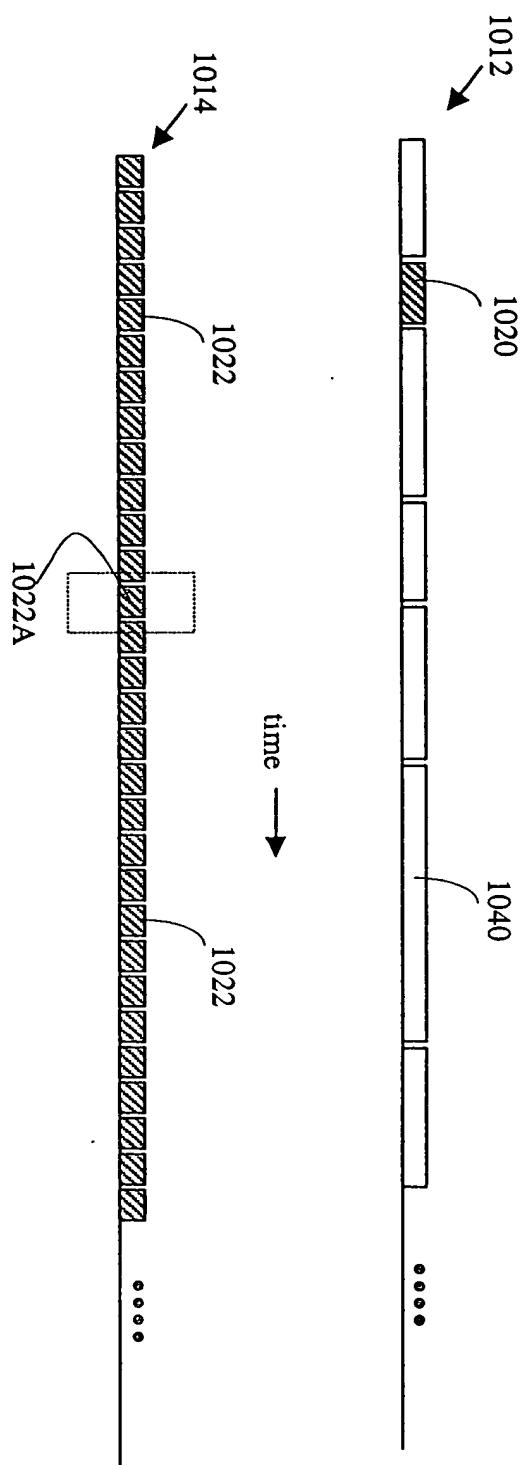


FIG. 10

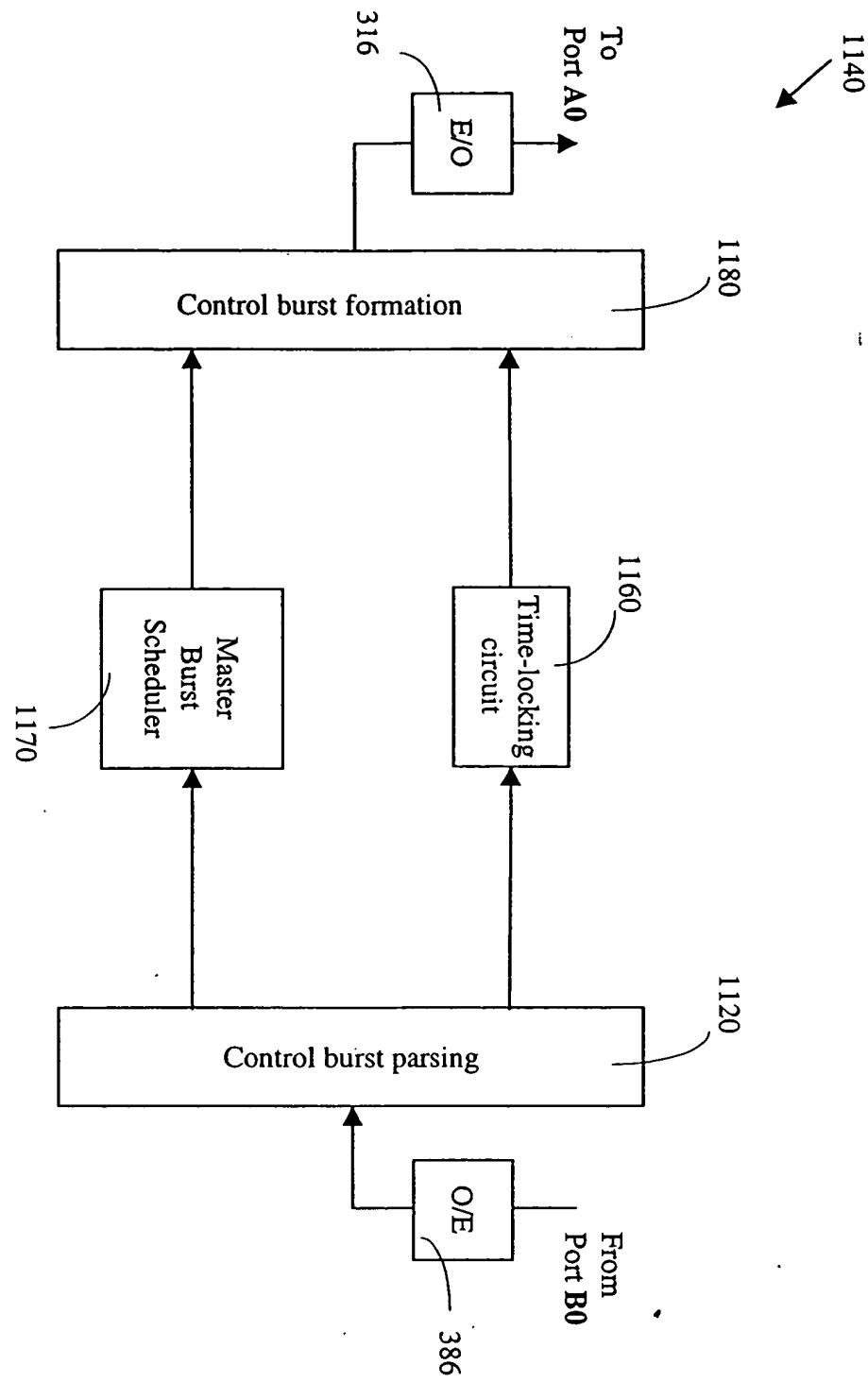


FIG. 11

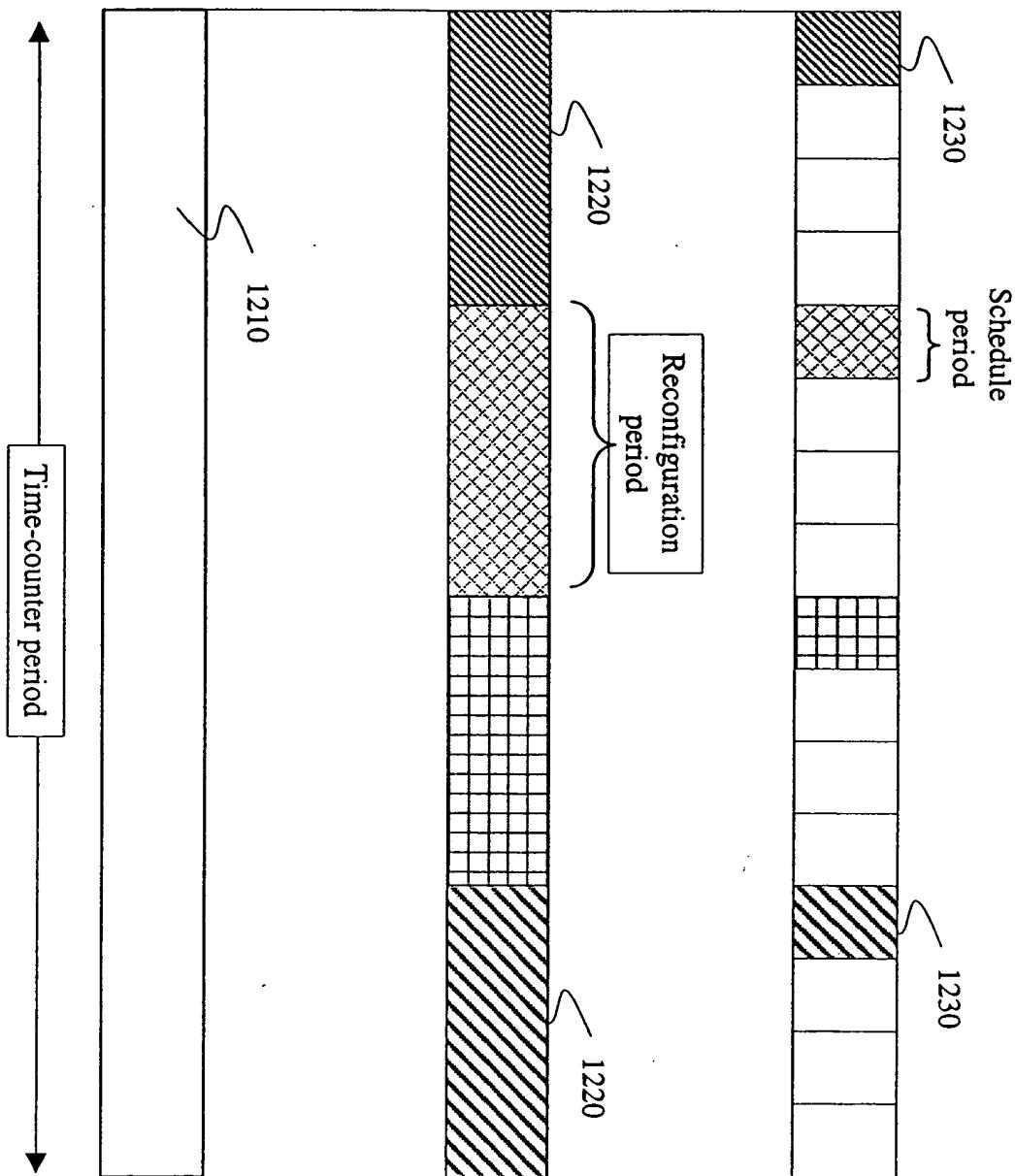


FIG. 12

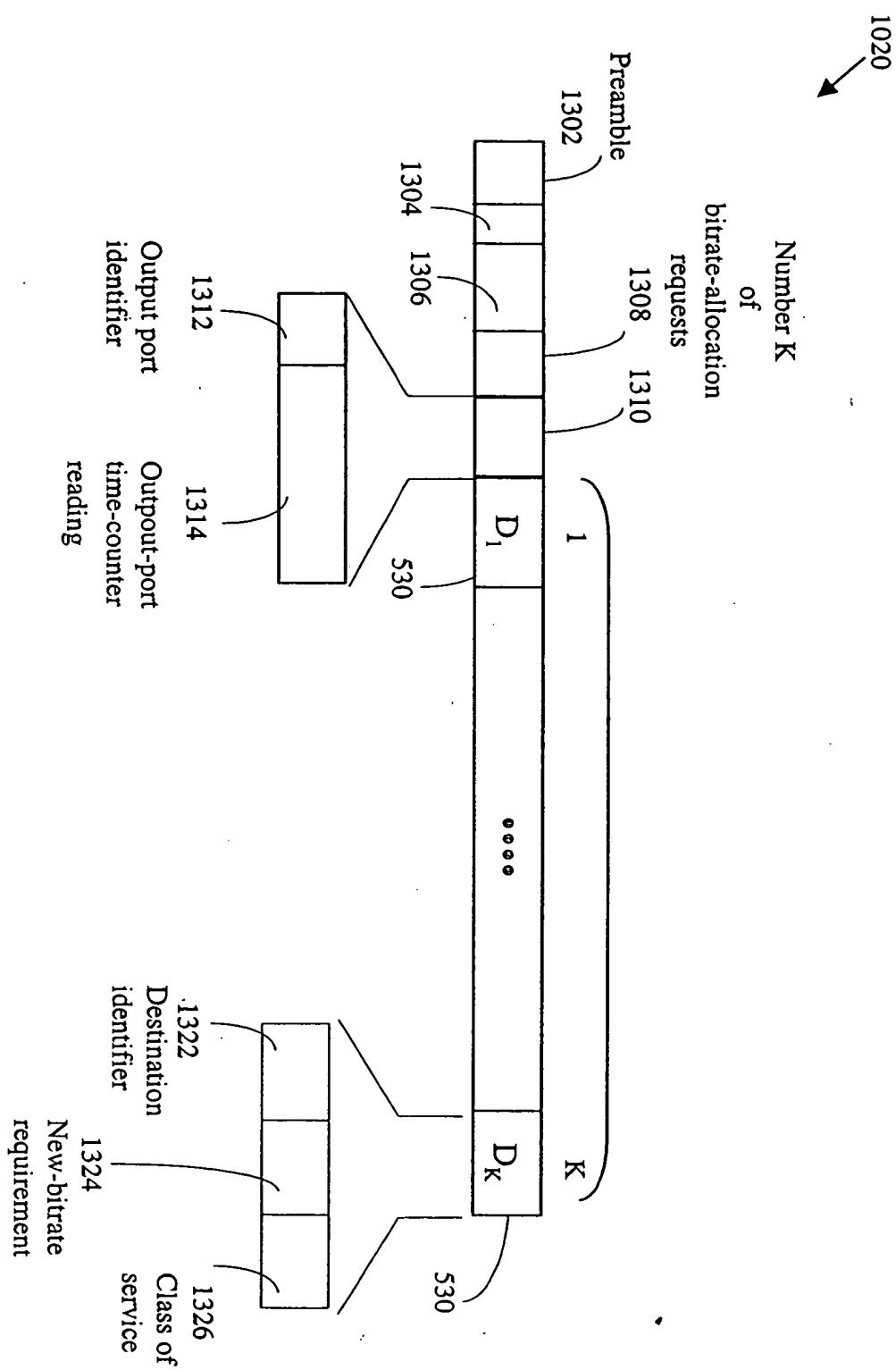


FIG. 13



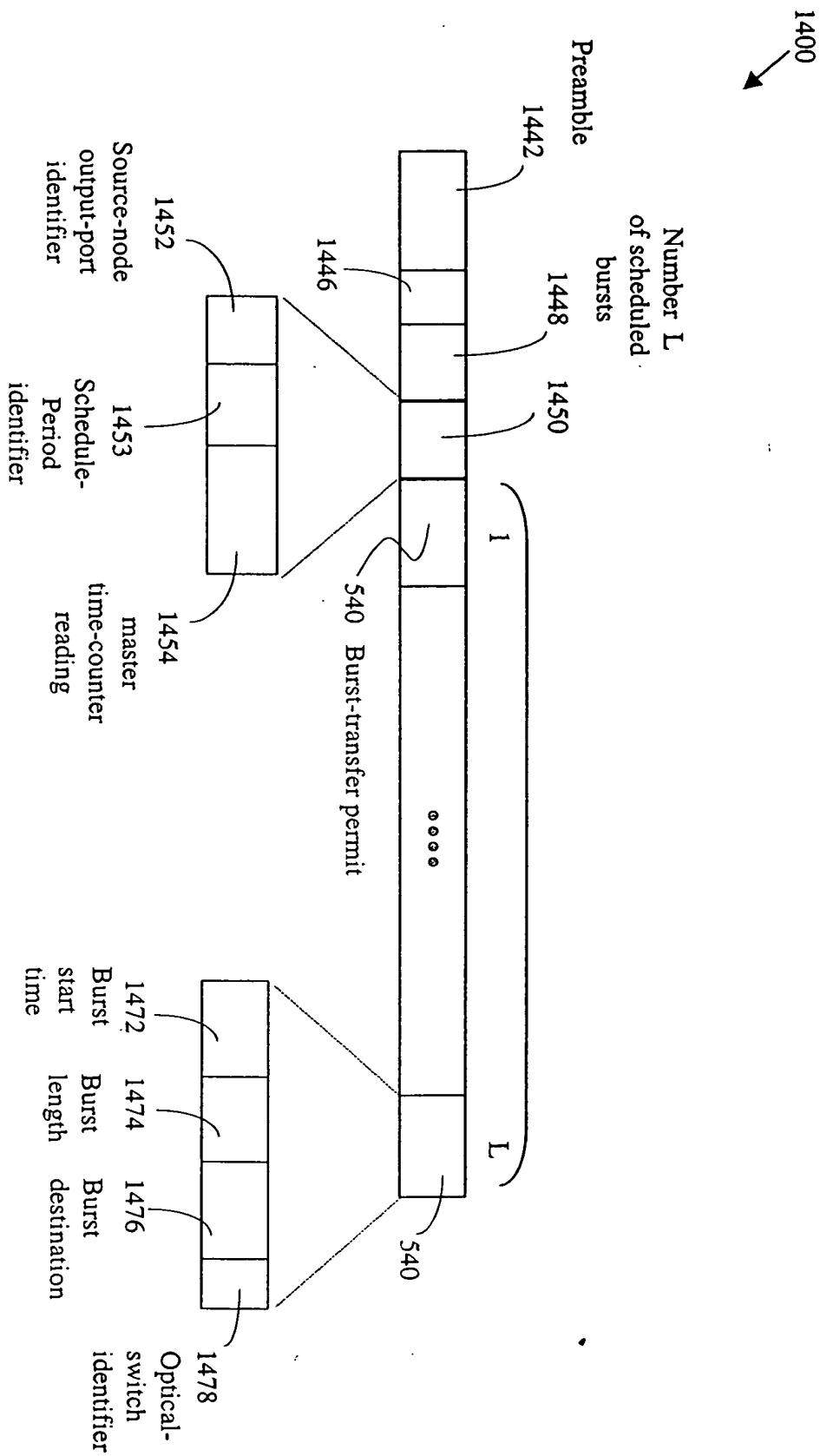


FIG. 14

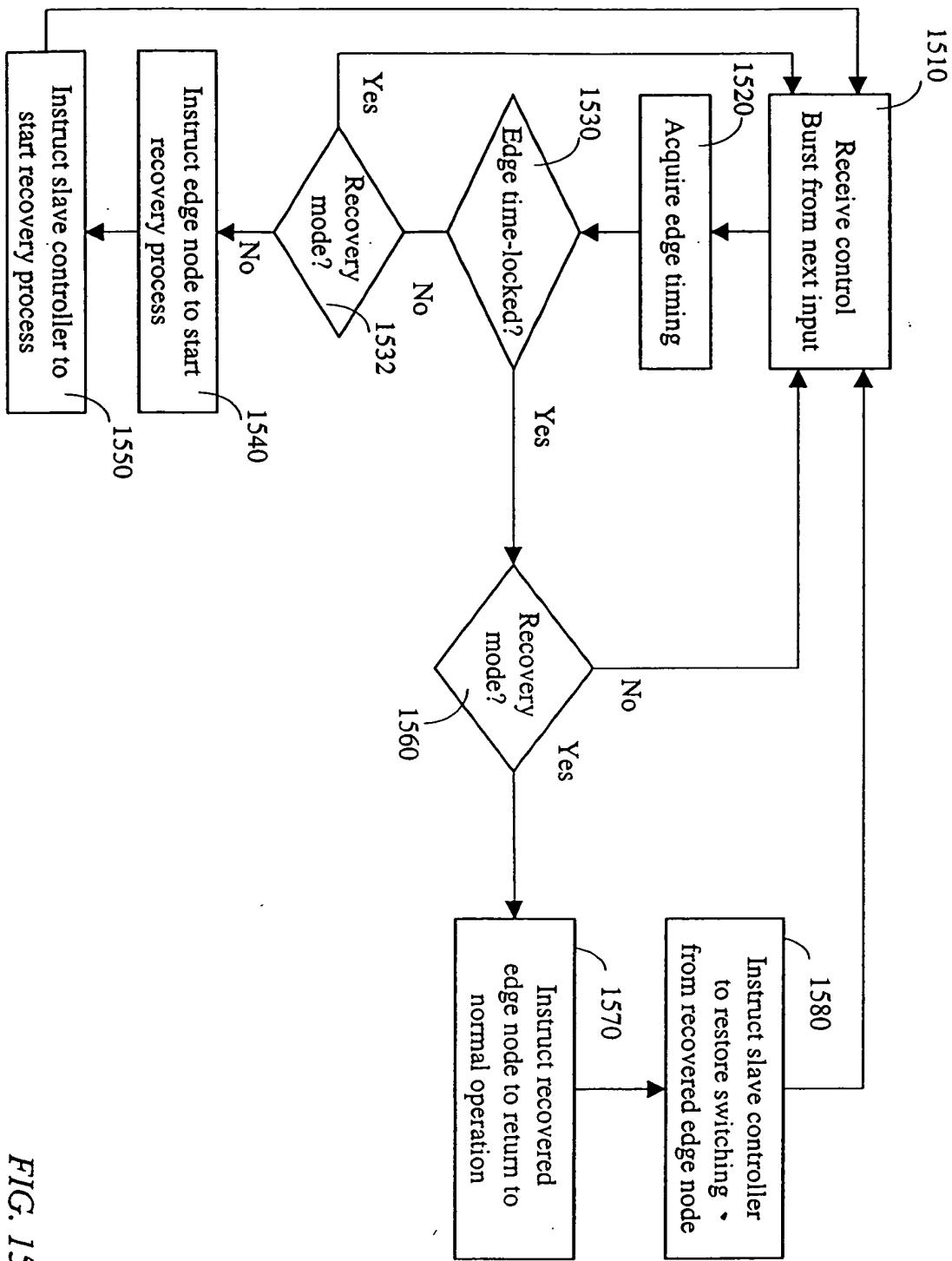


FIG. 15

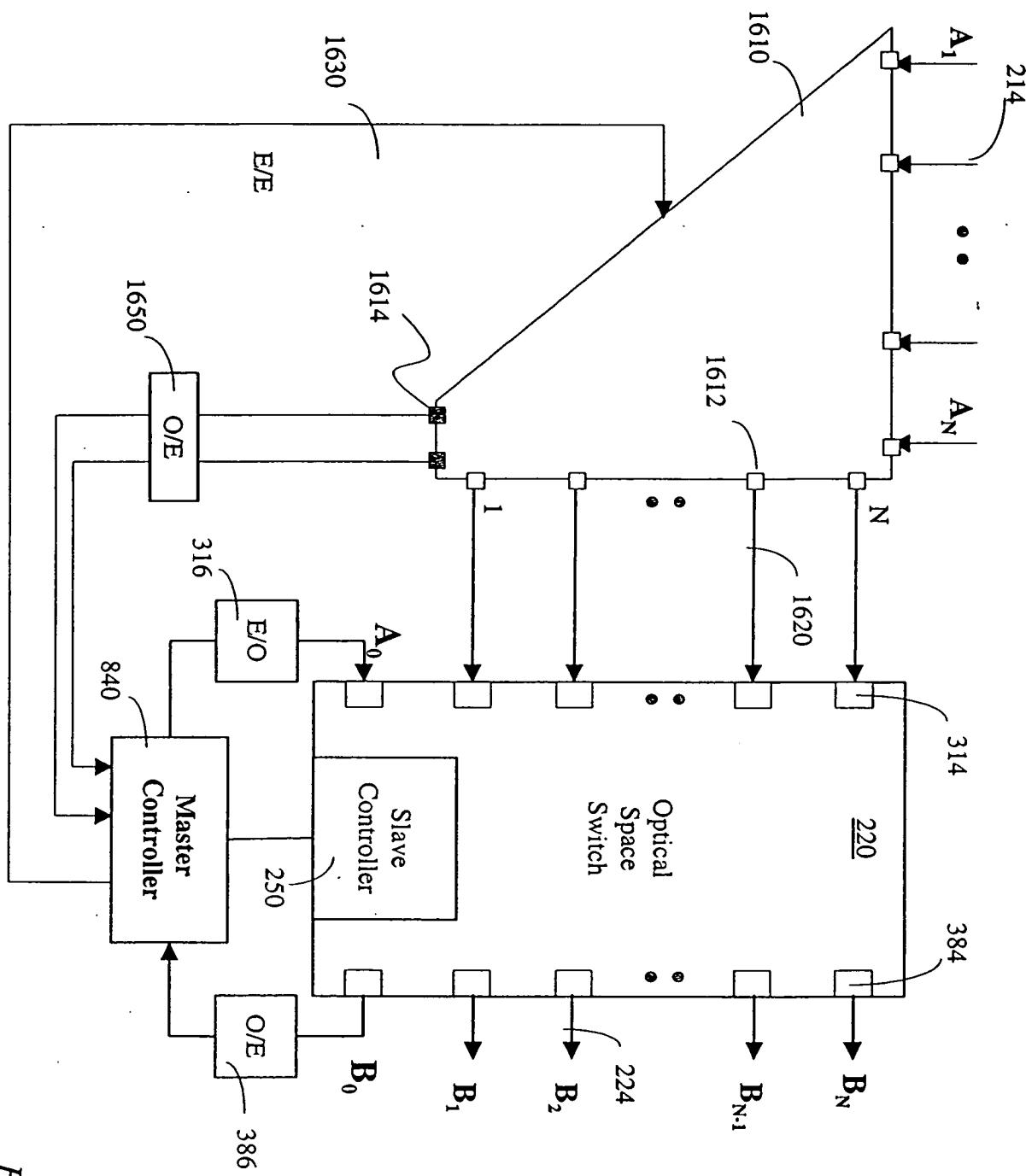


FIG. 16

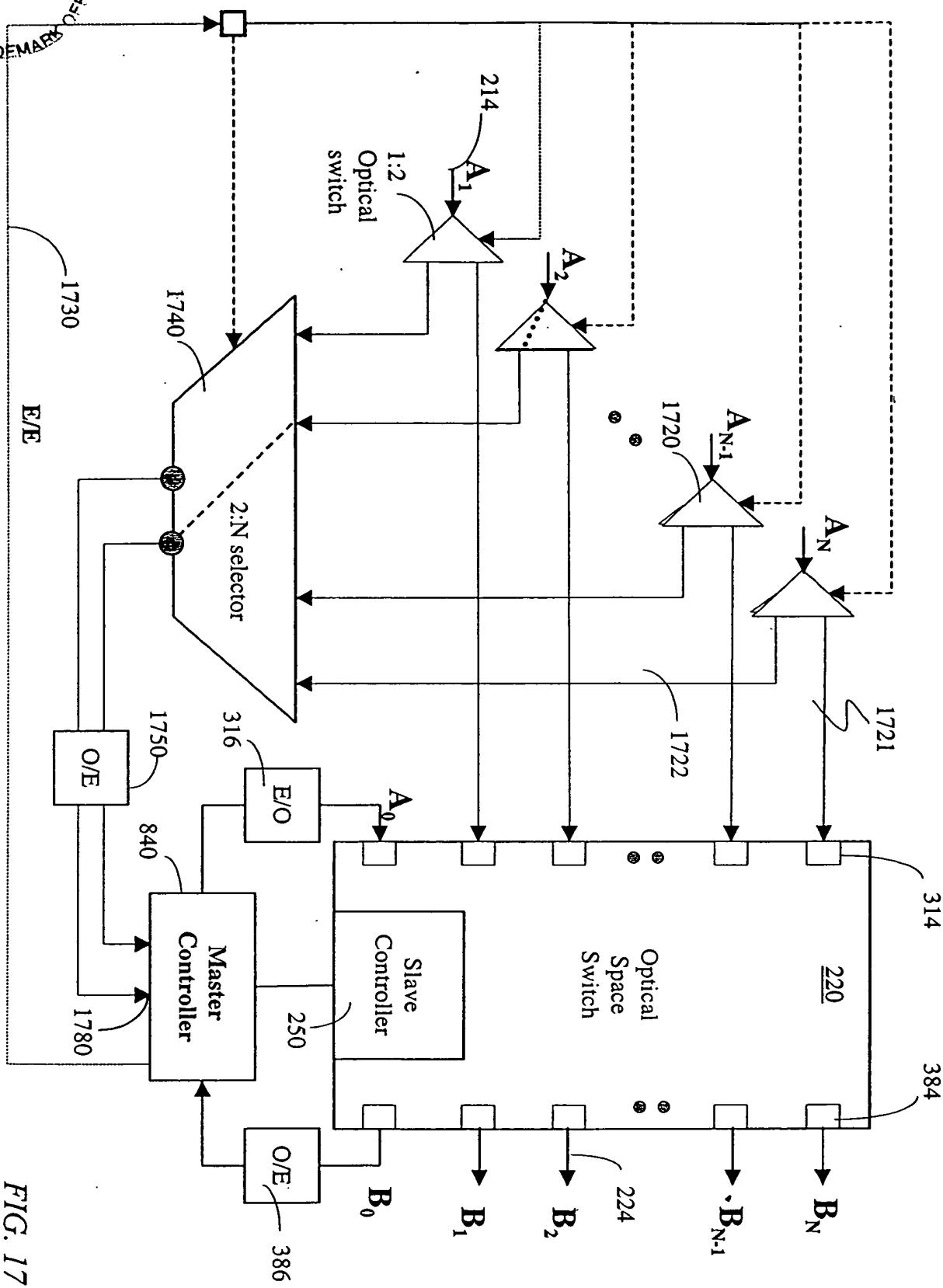


FIG. 17

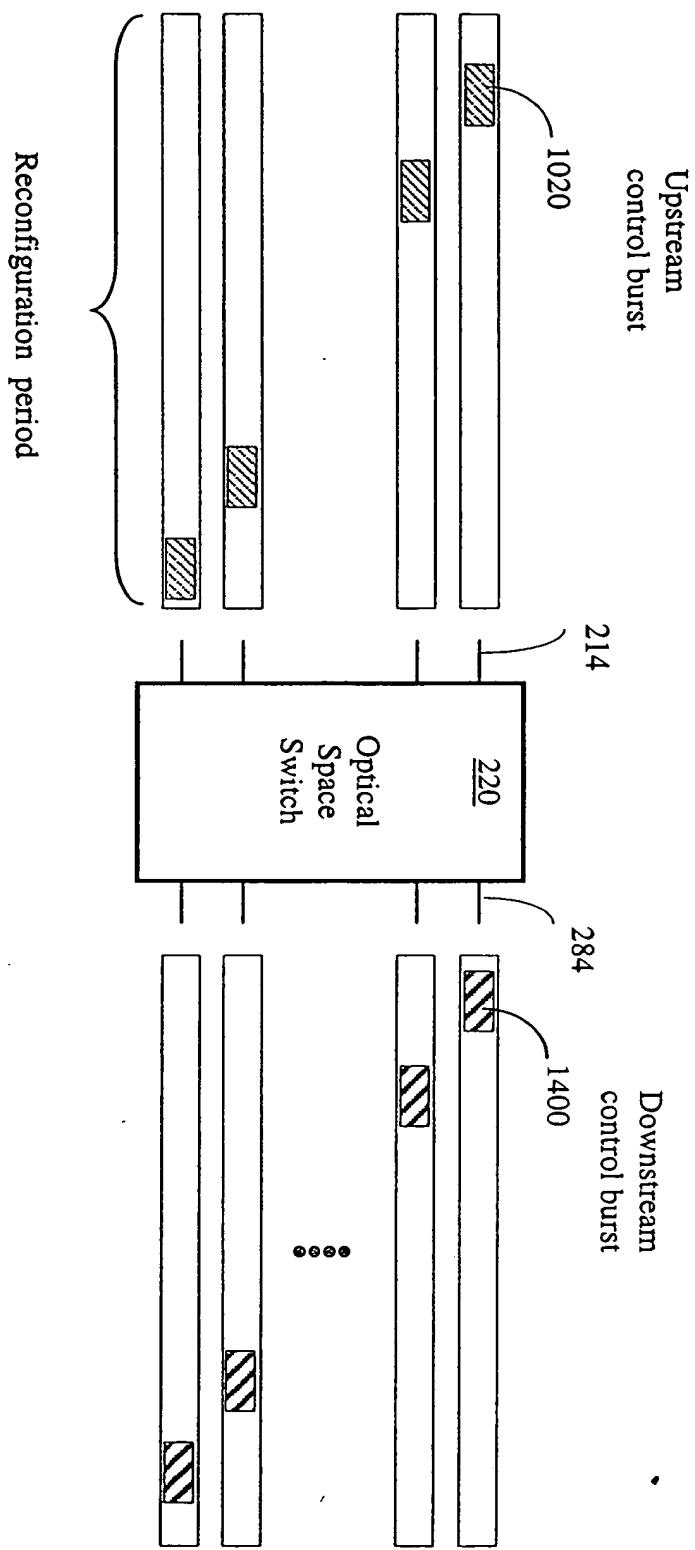


FIG. 18

Time-counter cycle (master cycle) Period:  $2^w$  time slots

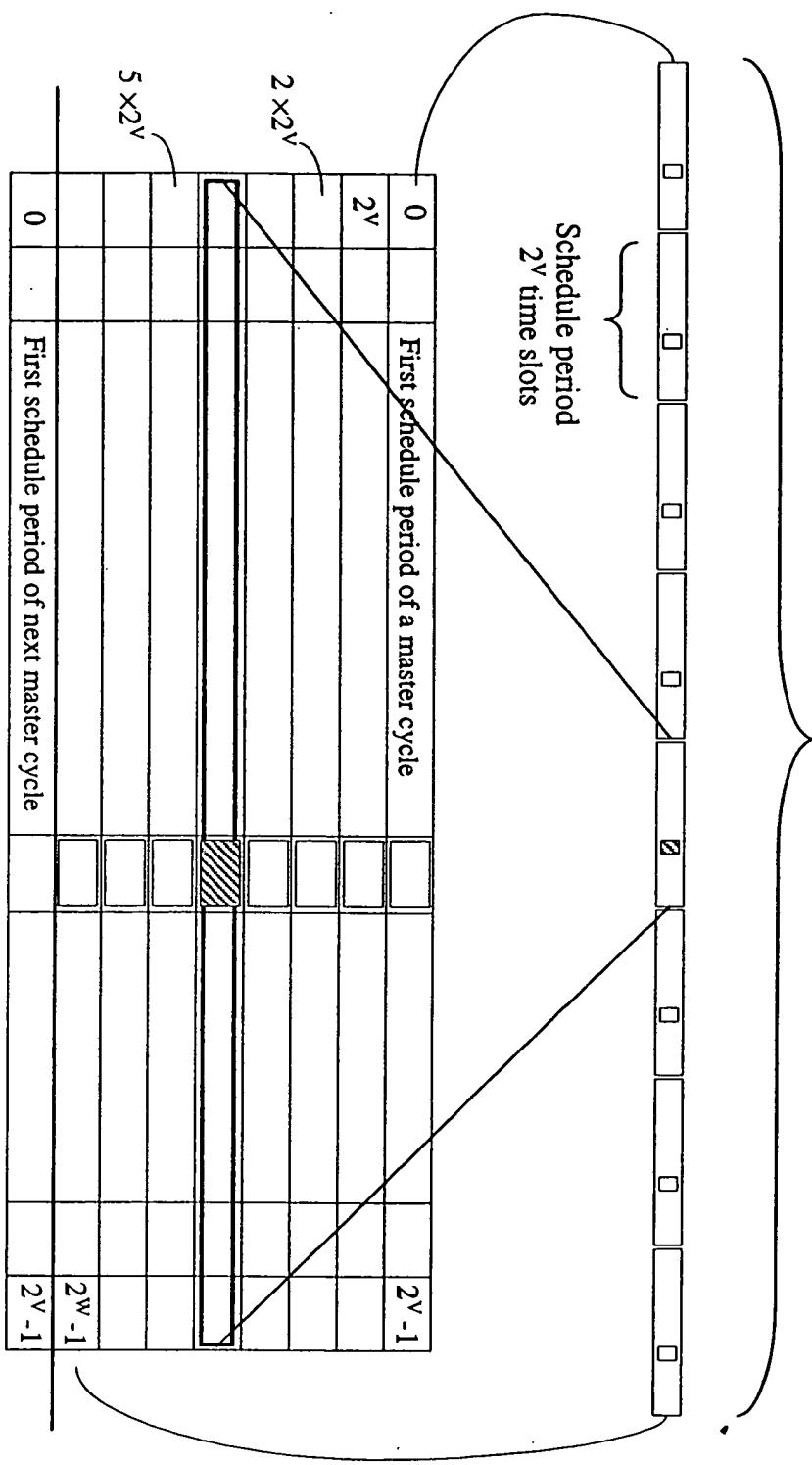


FIG. 19



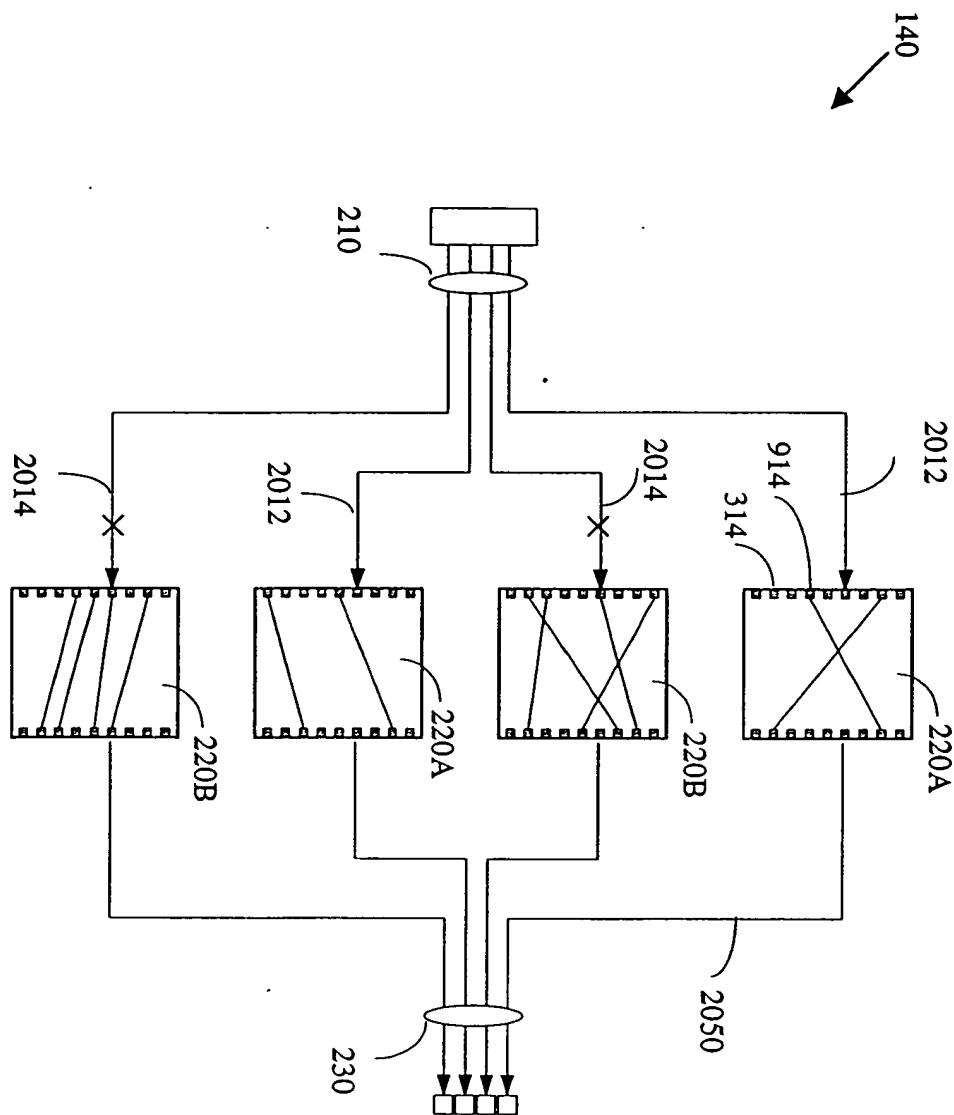


FIG. 20

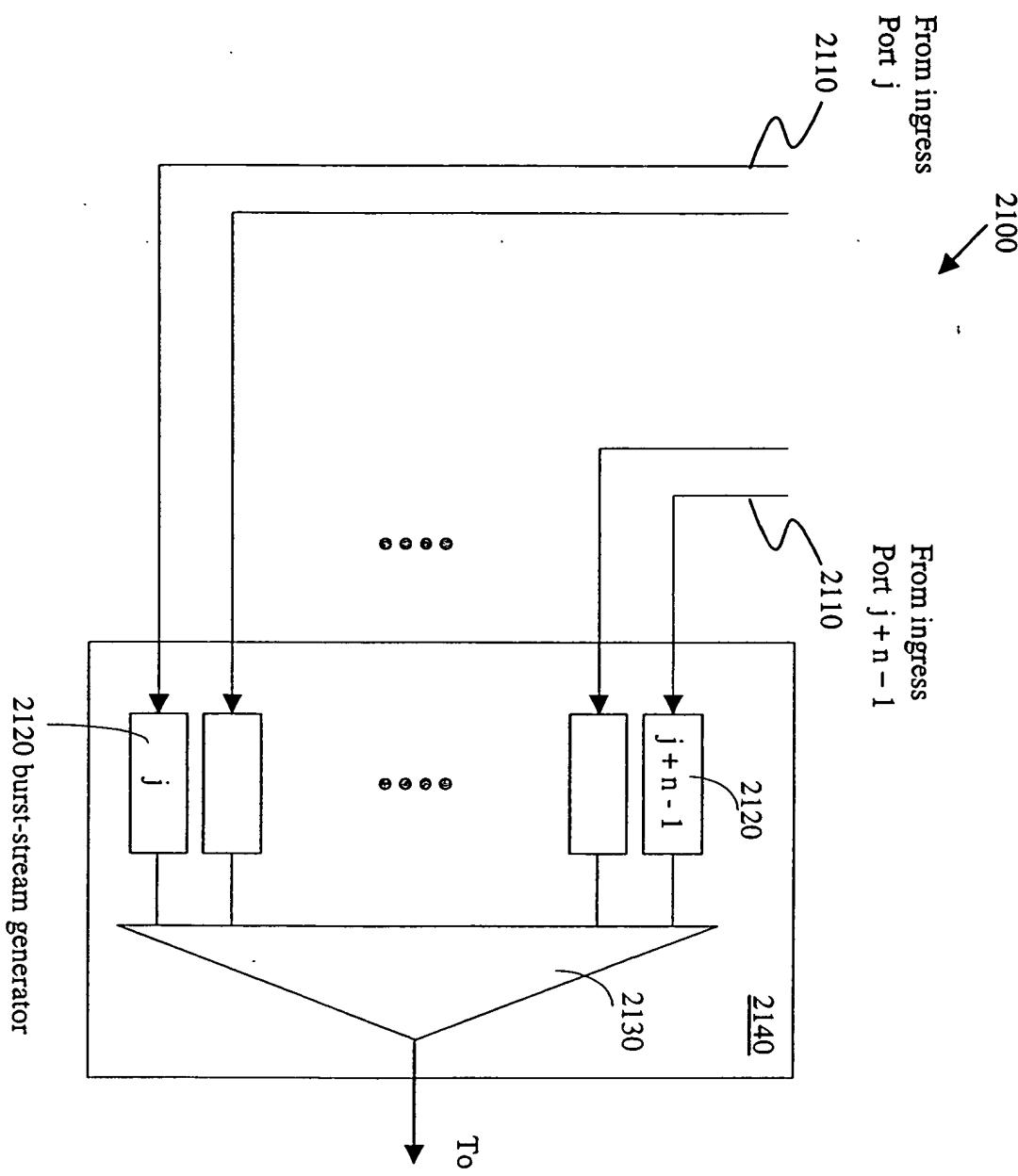


FIG. 21

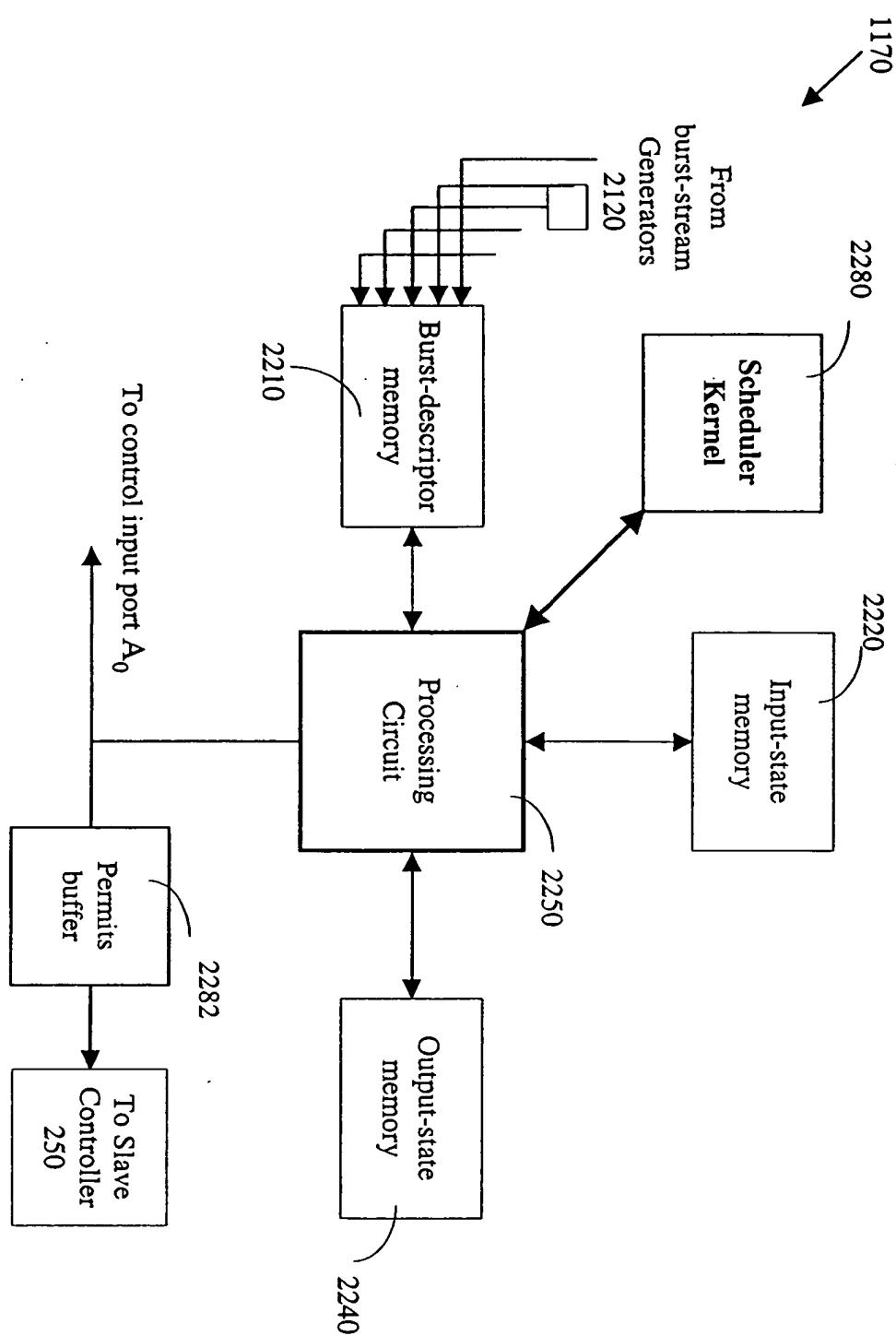


FIG. 22

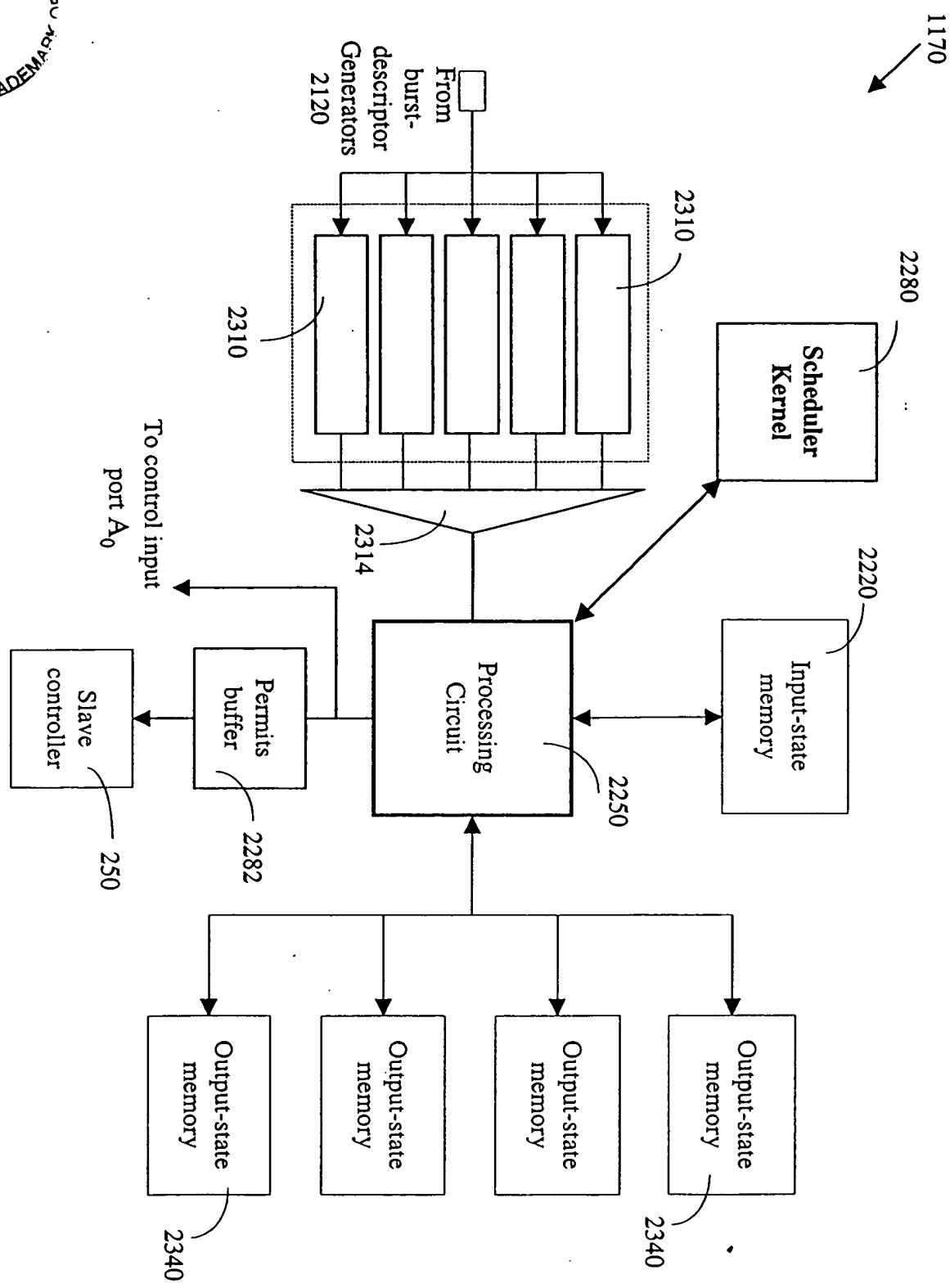


FIG. 23

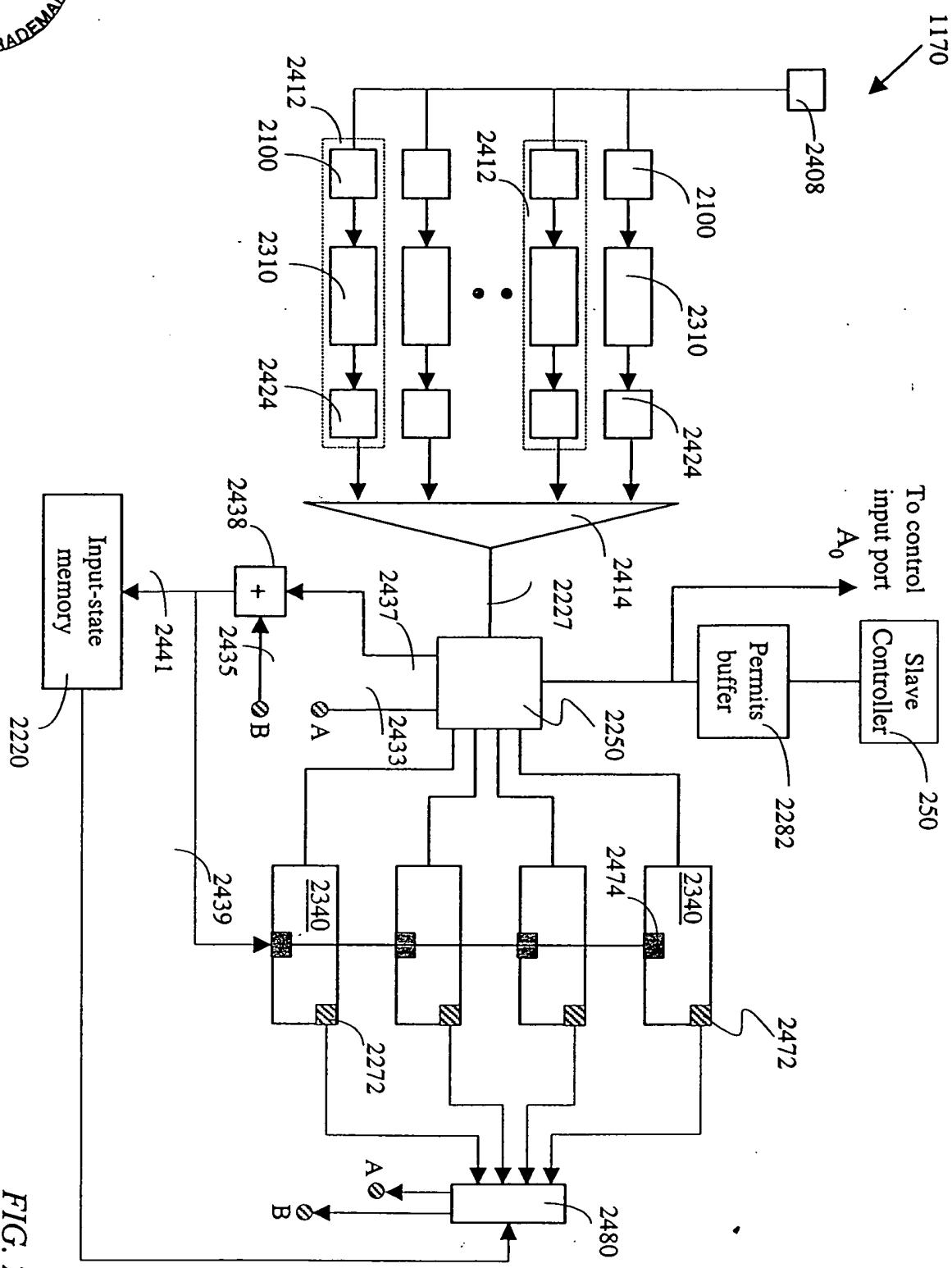


FIG. 24

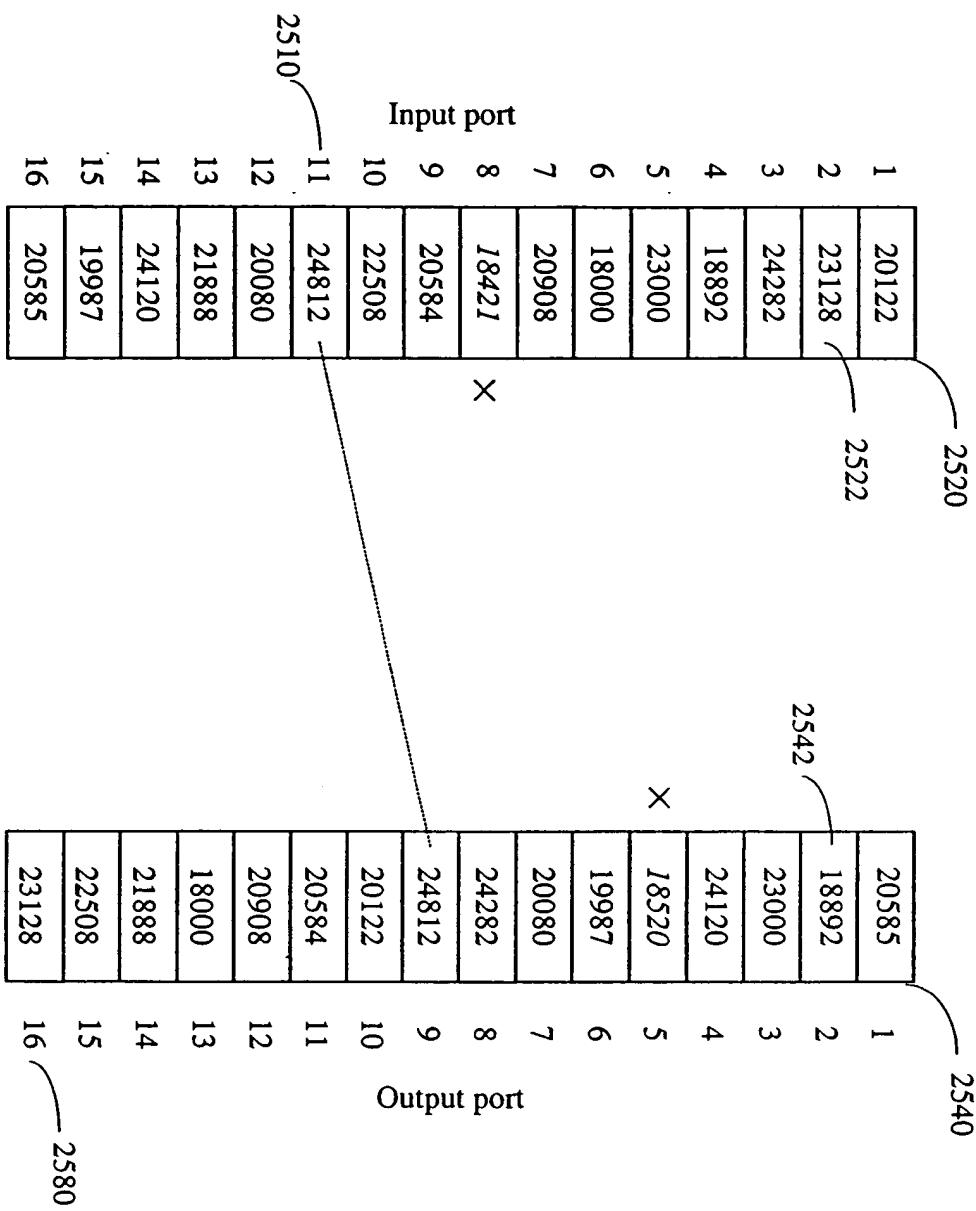
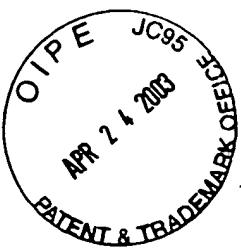


FIG. 25

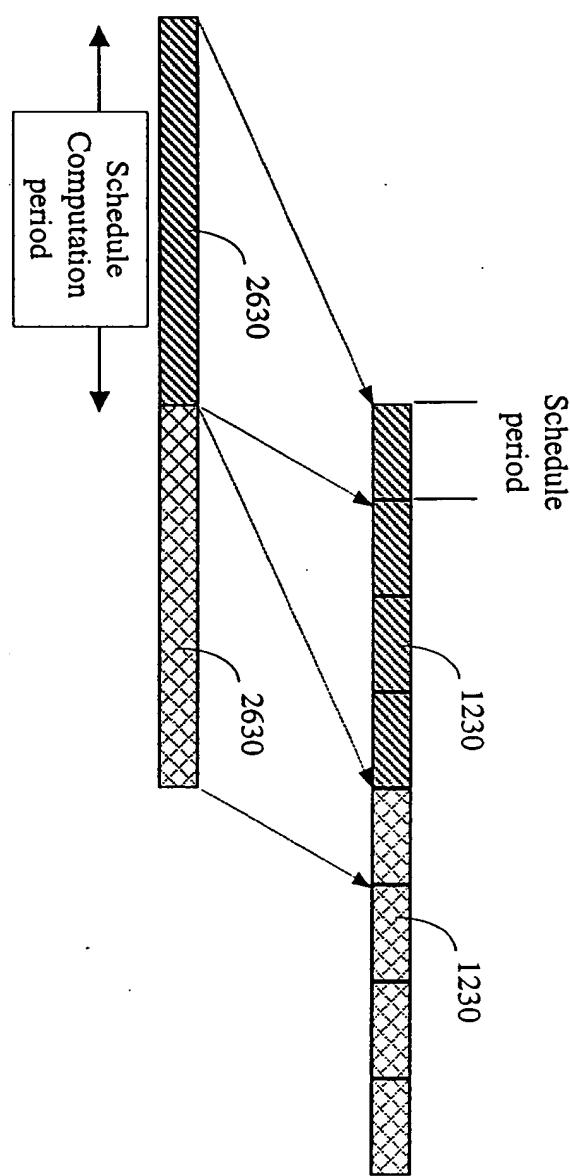


FIG. 26

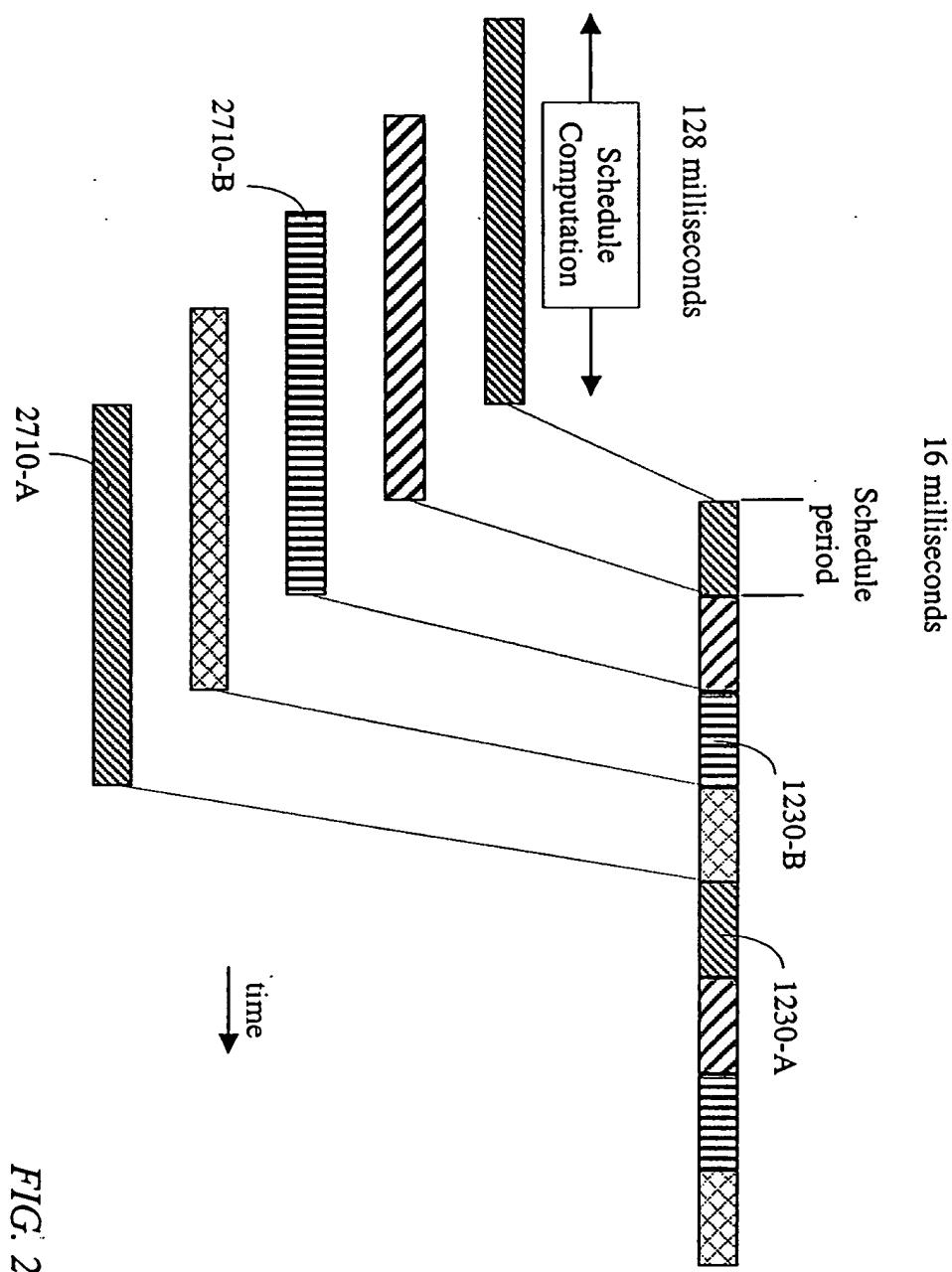


FIG. 27

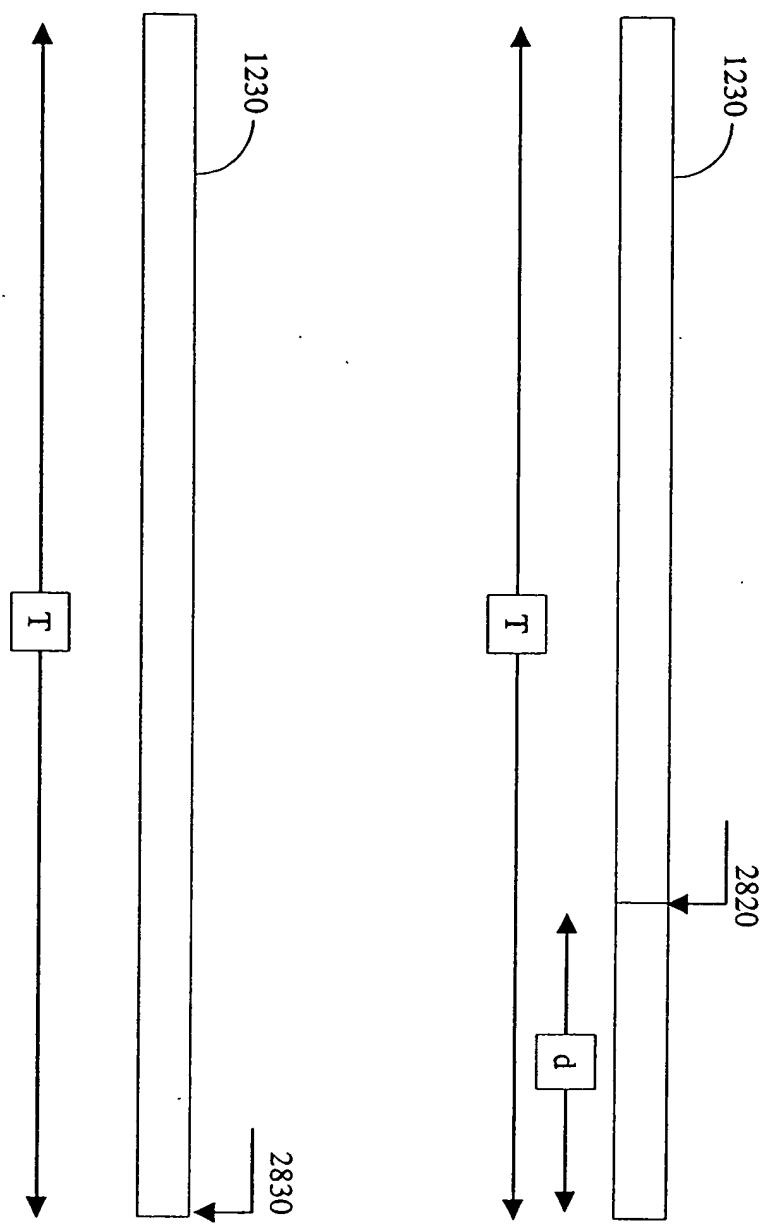


FIG. 28

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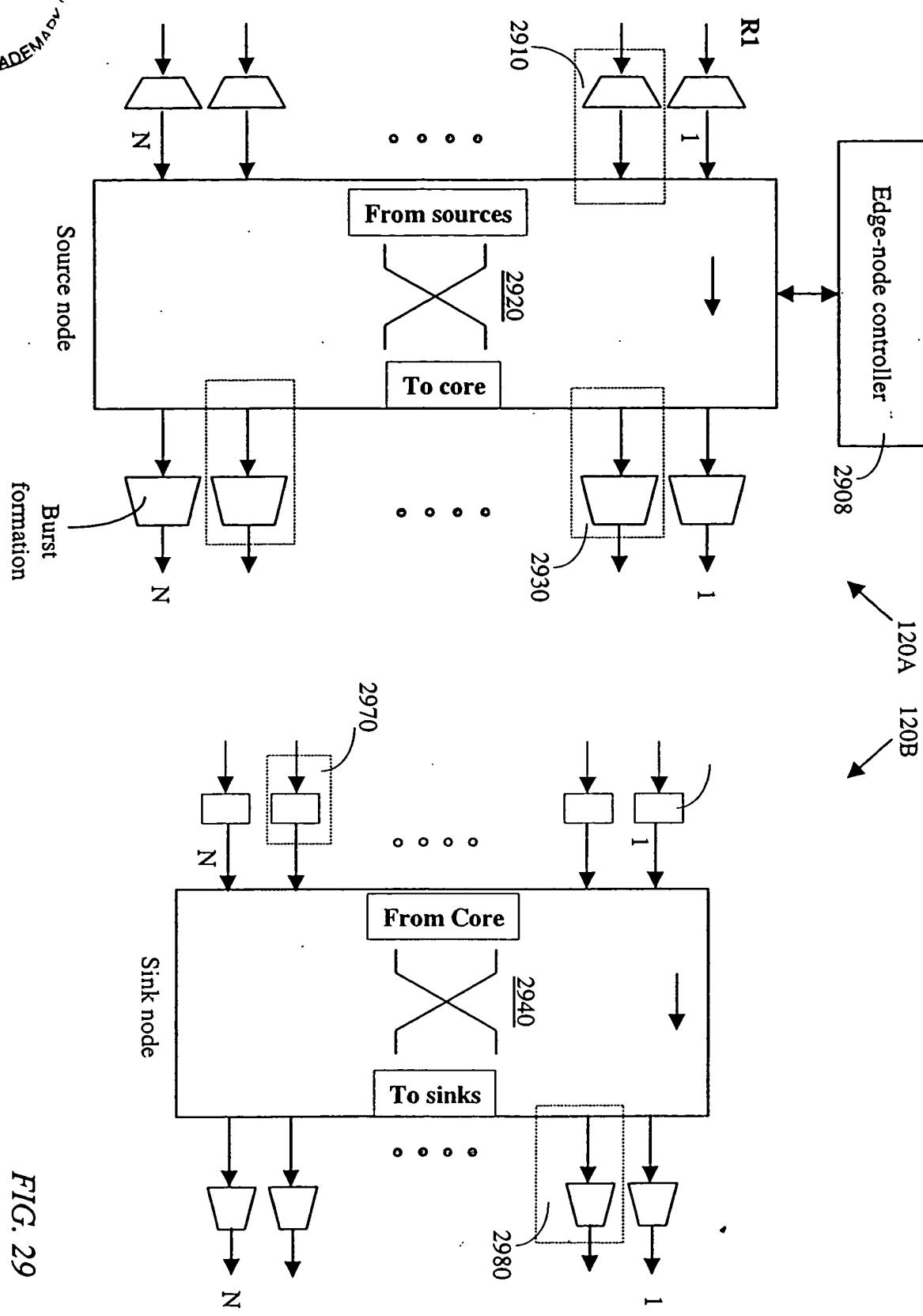


FIG. 29

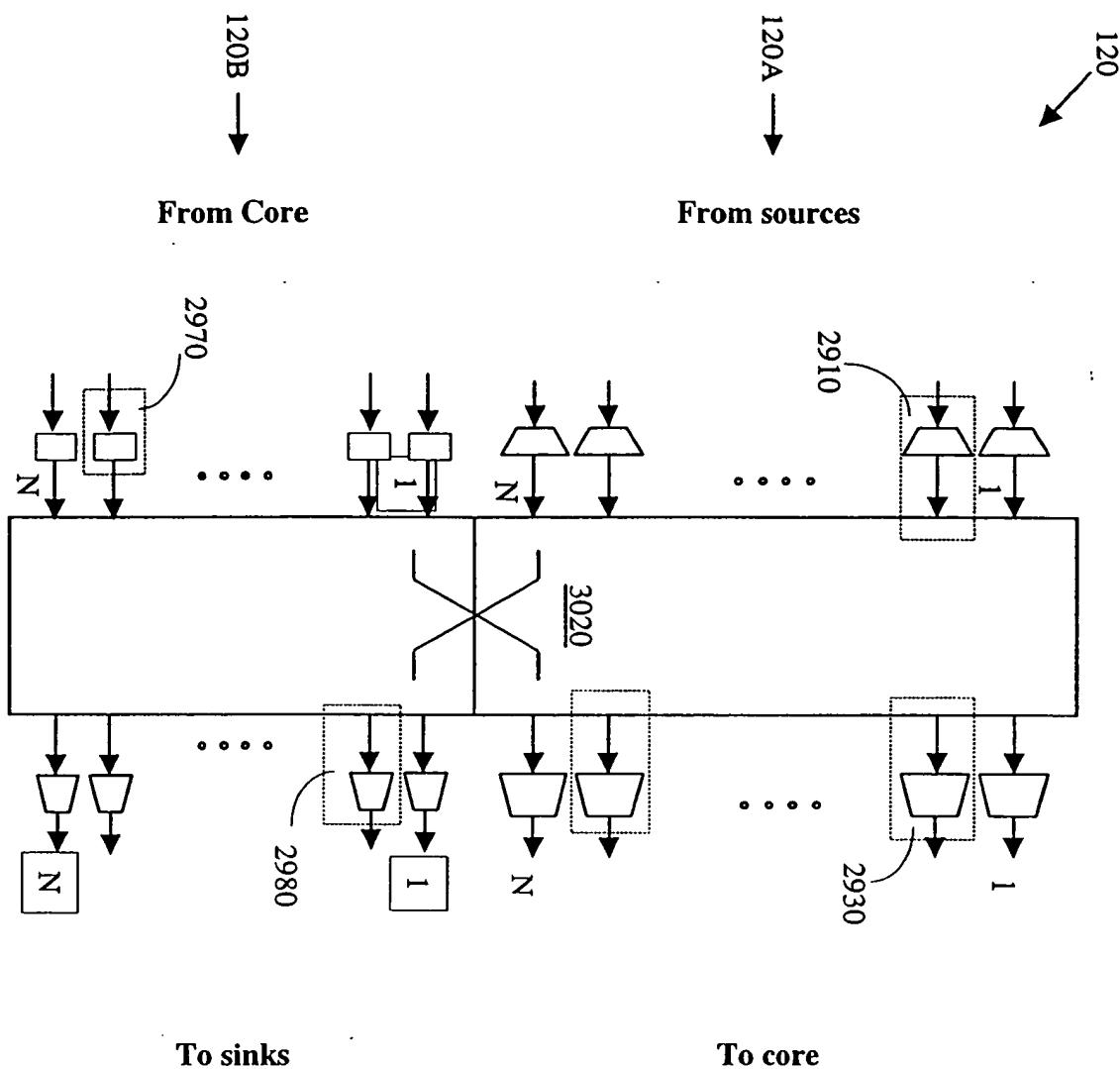


FIG. 30

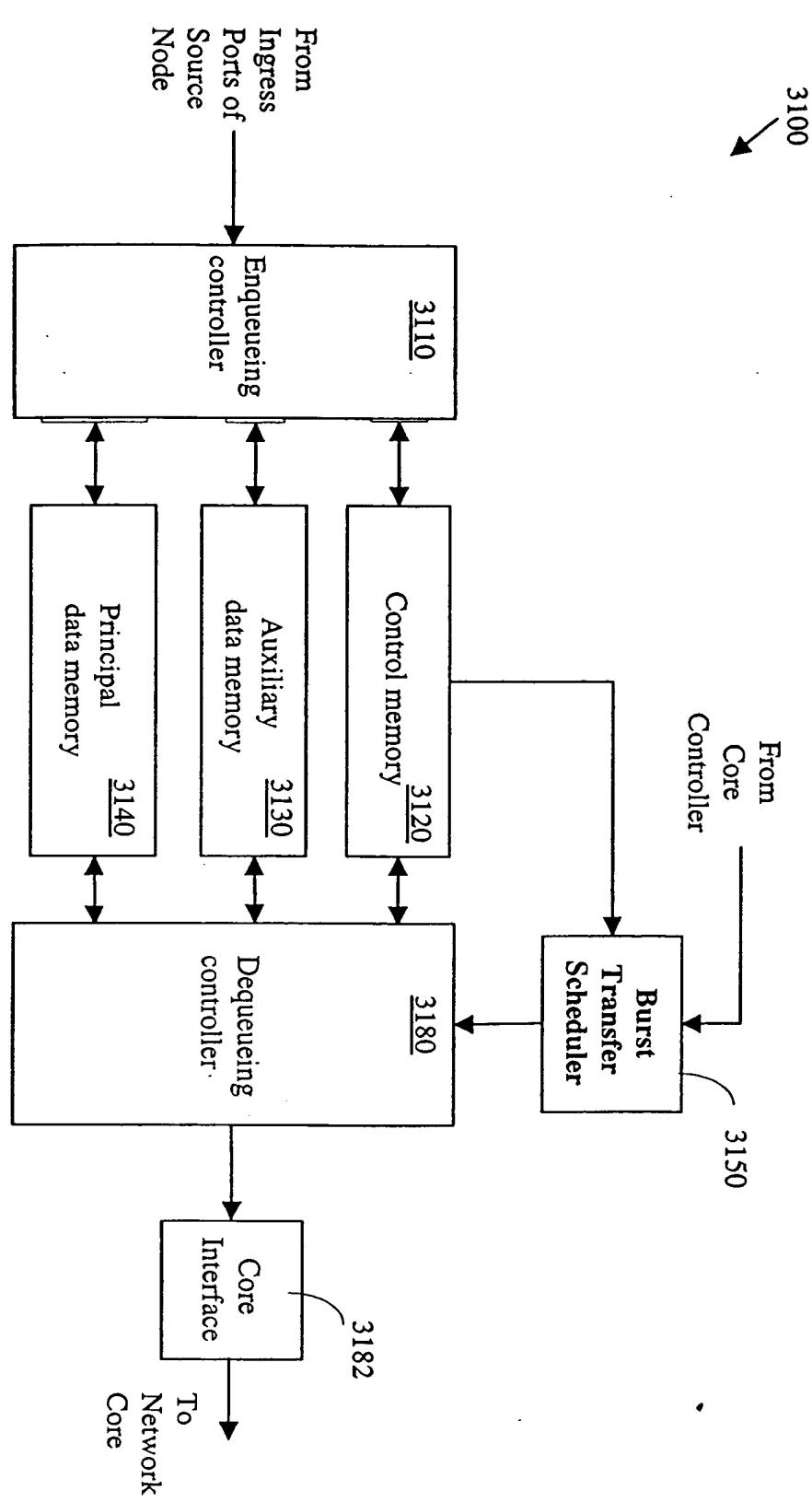


FIG. 31

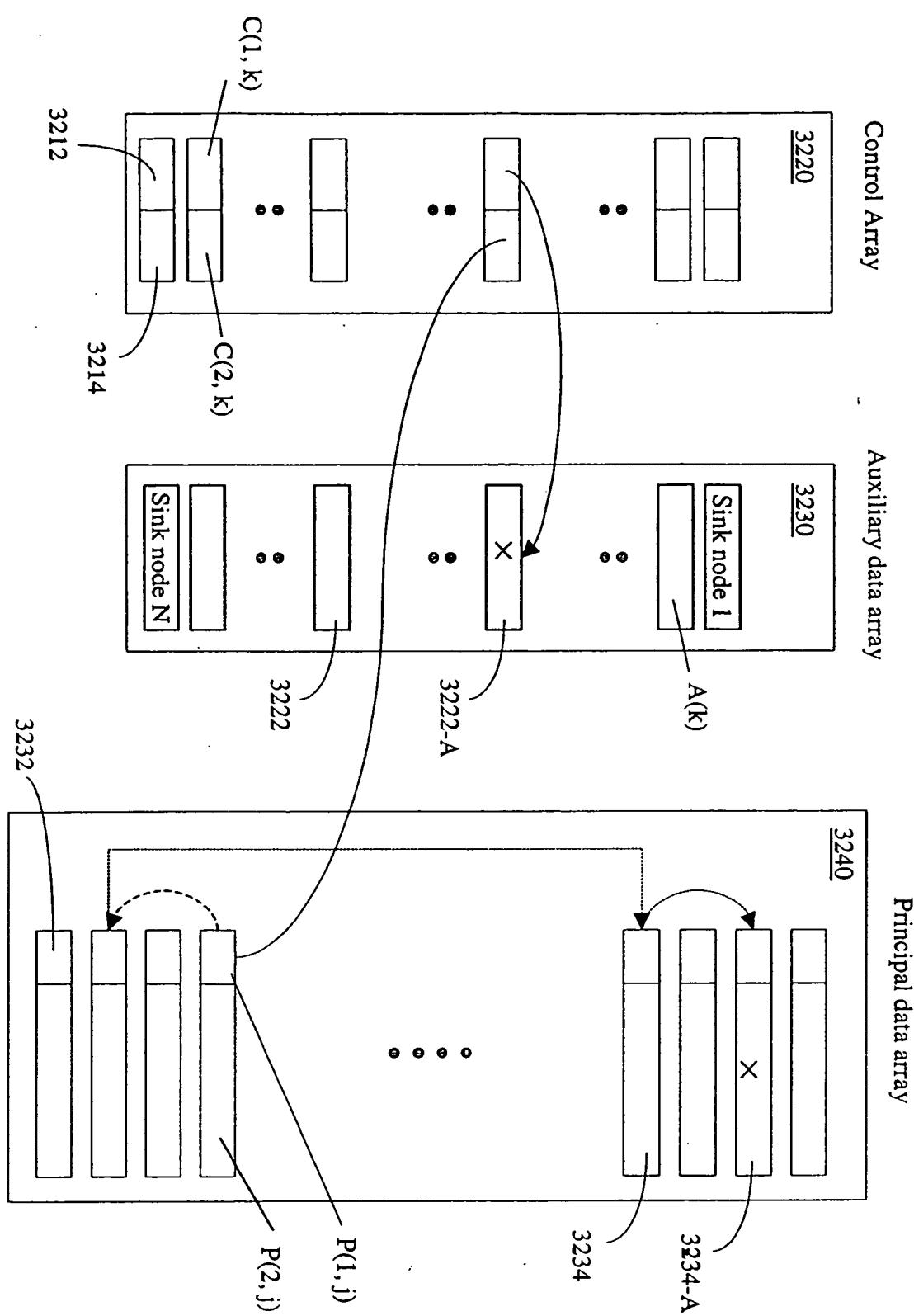


FIG. 32



Receive segment:  
destination k

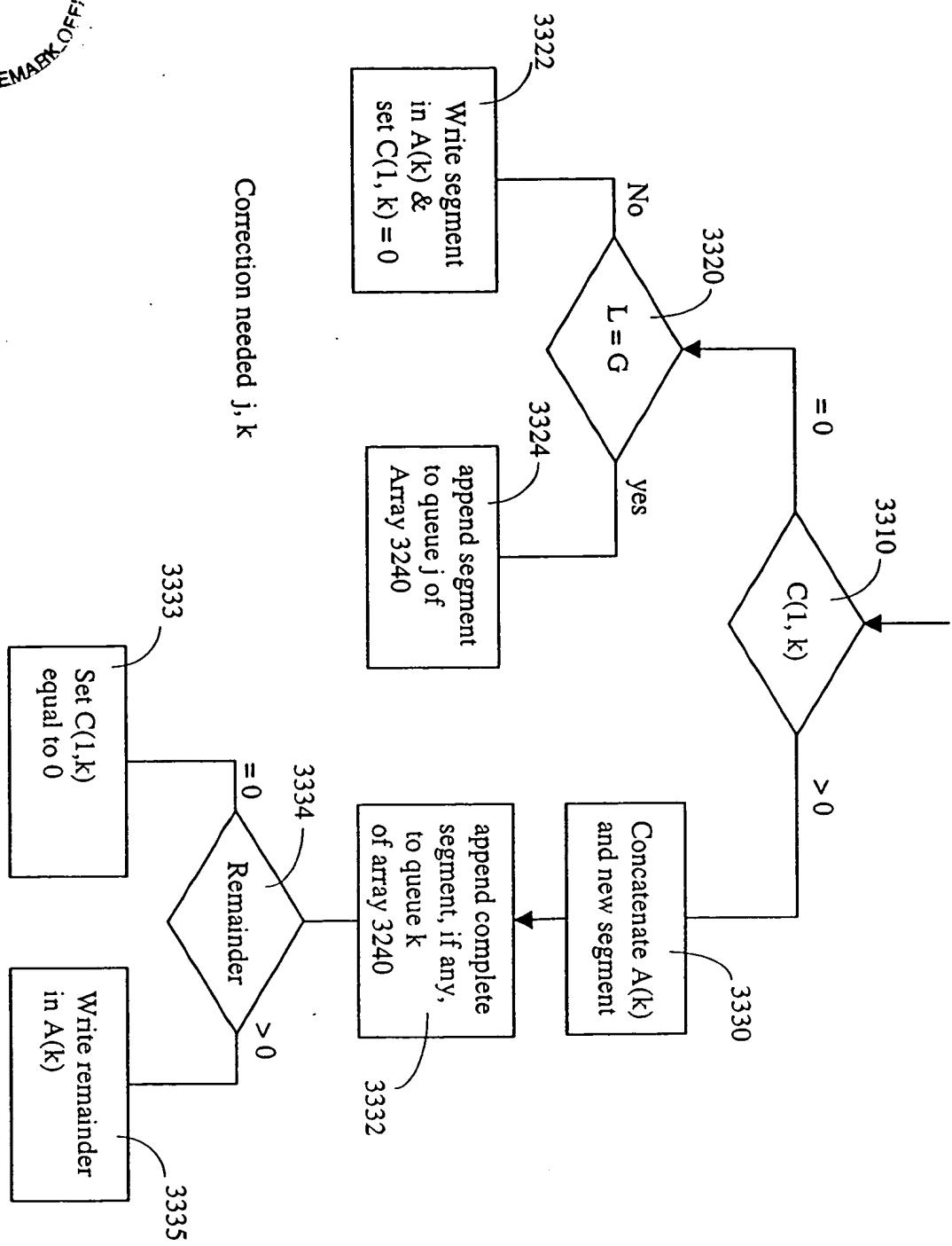


FIG. 33

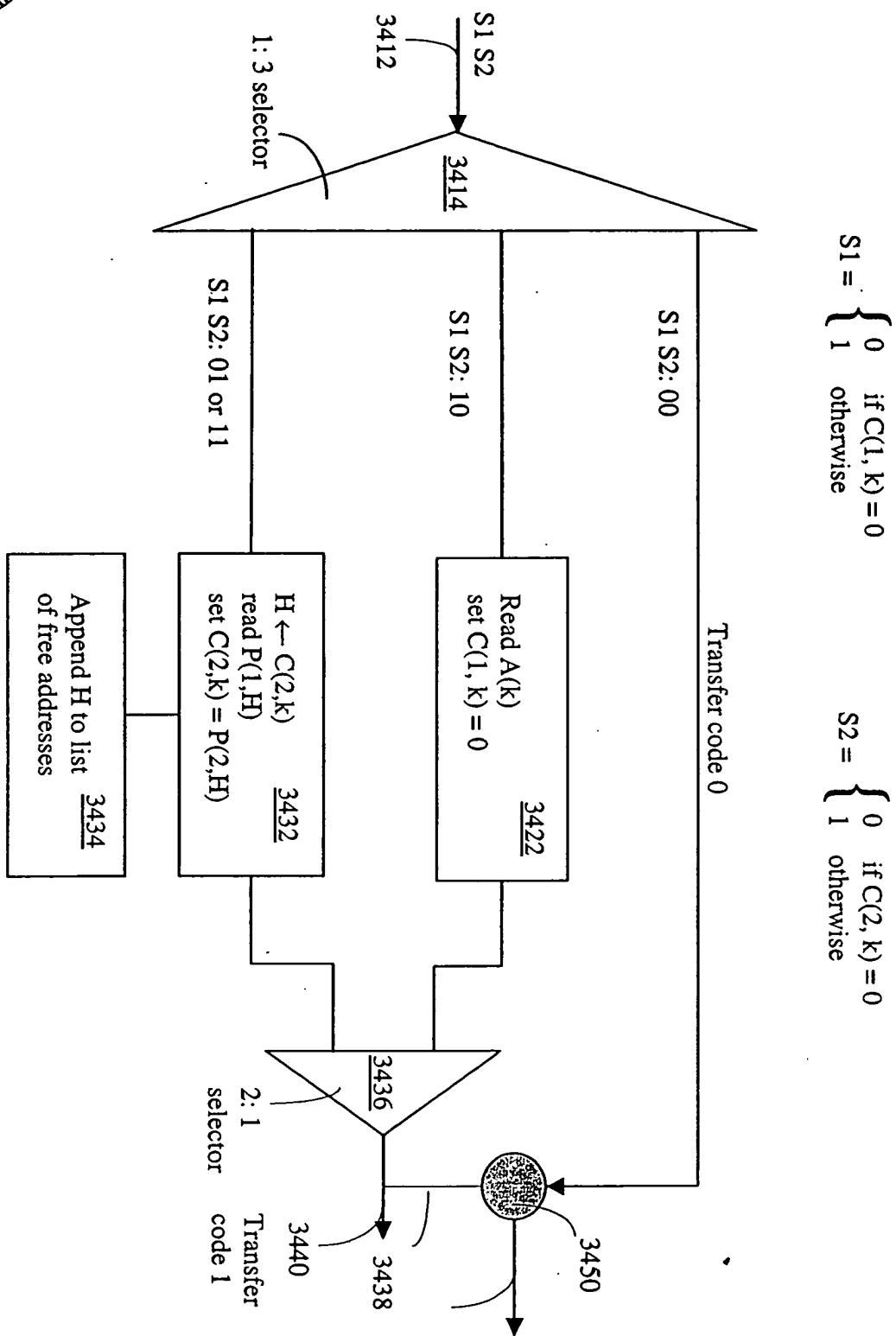


FIG. 34